

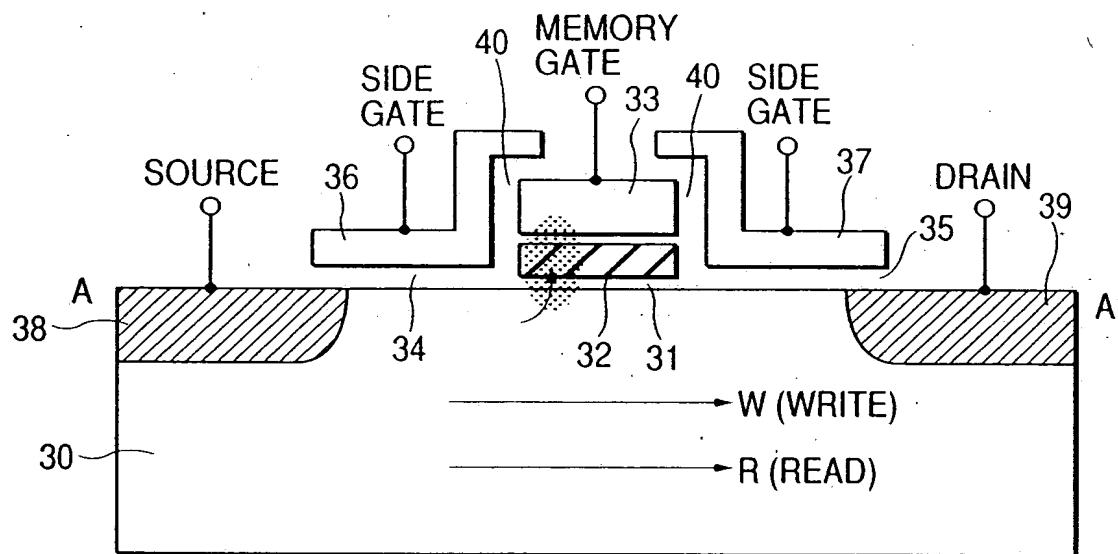
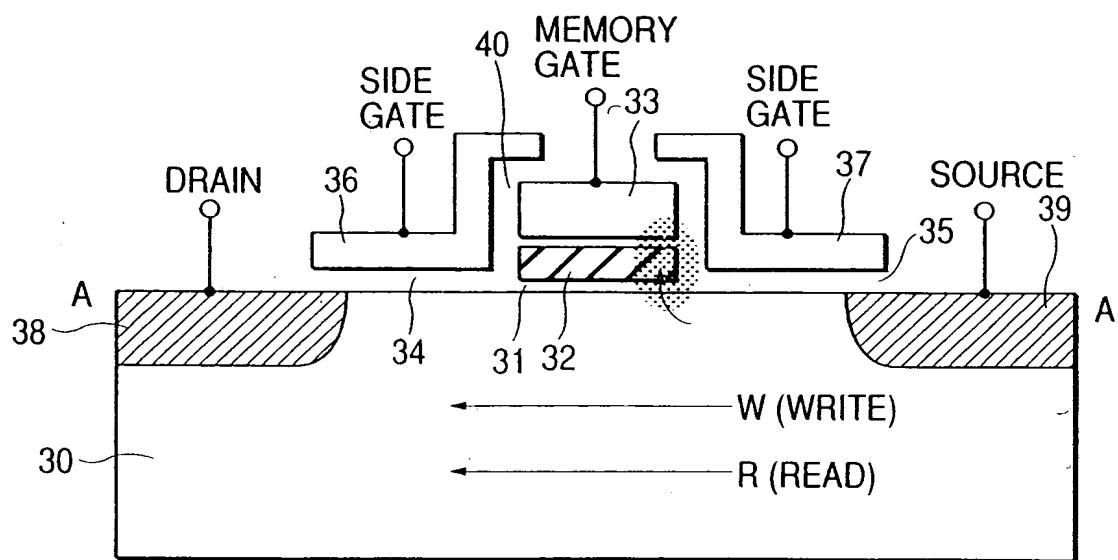
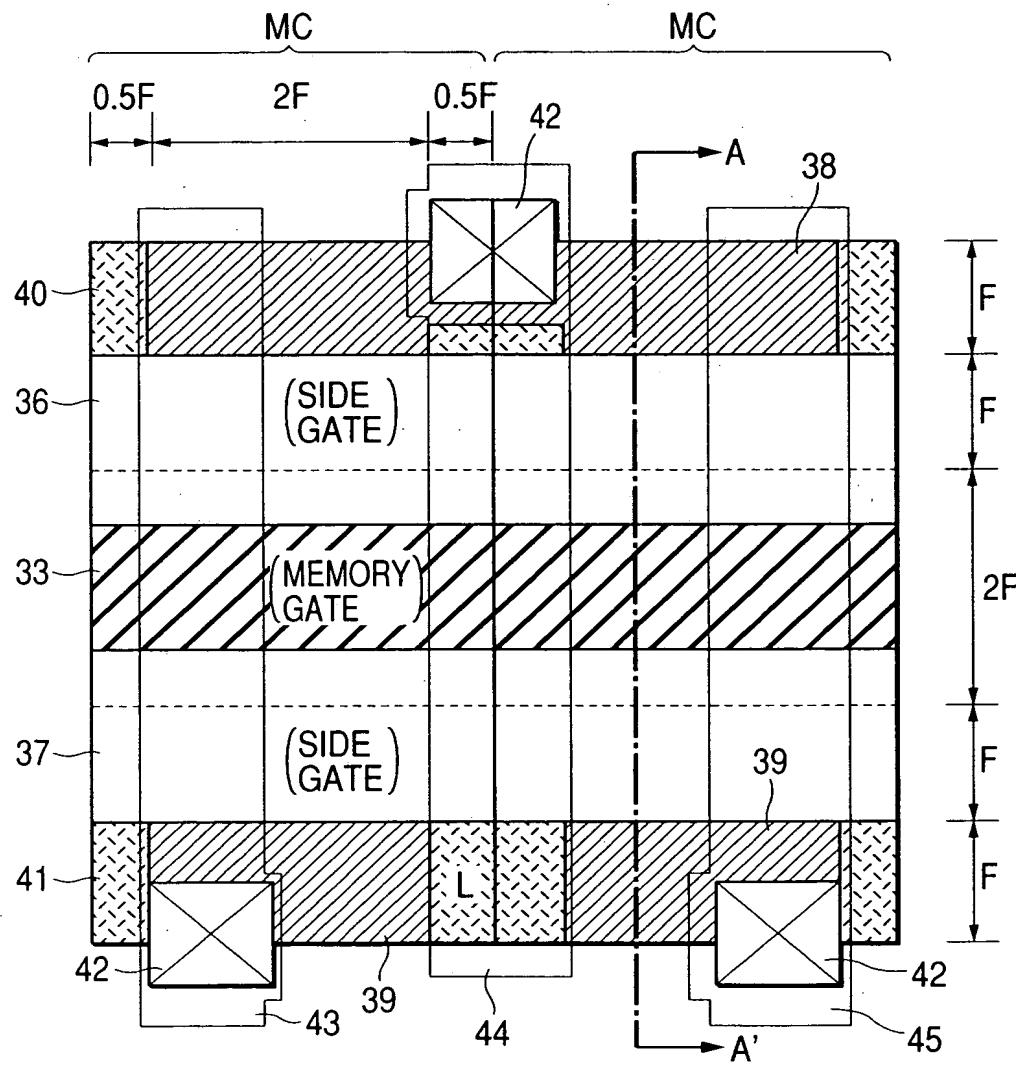
FIG. 1(A)**FIG. 1(B)**

FIG. 2



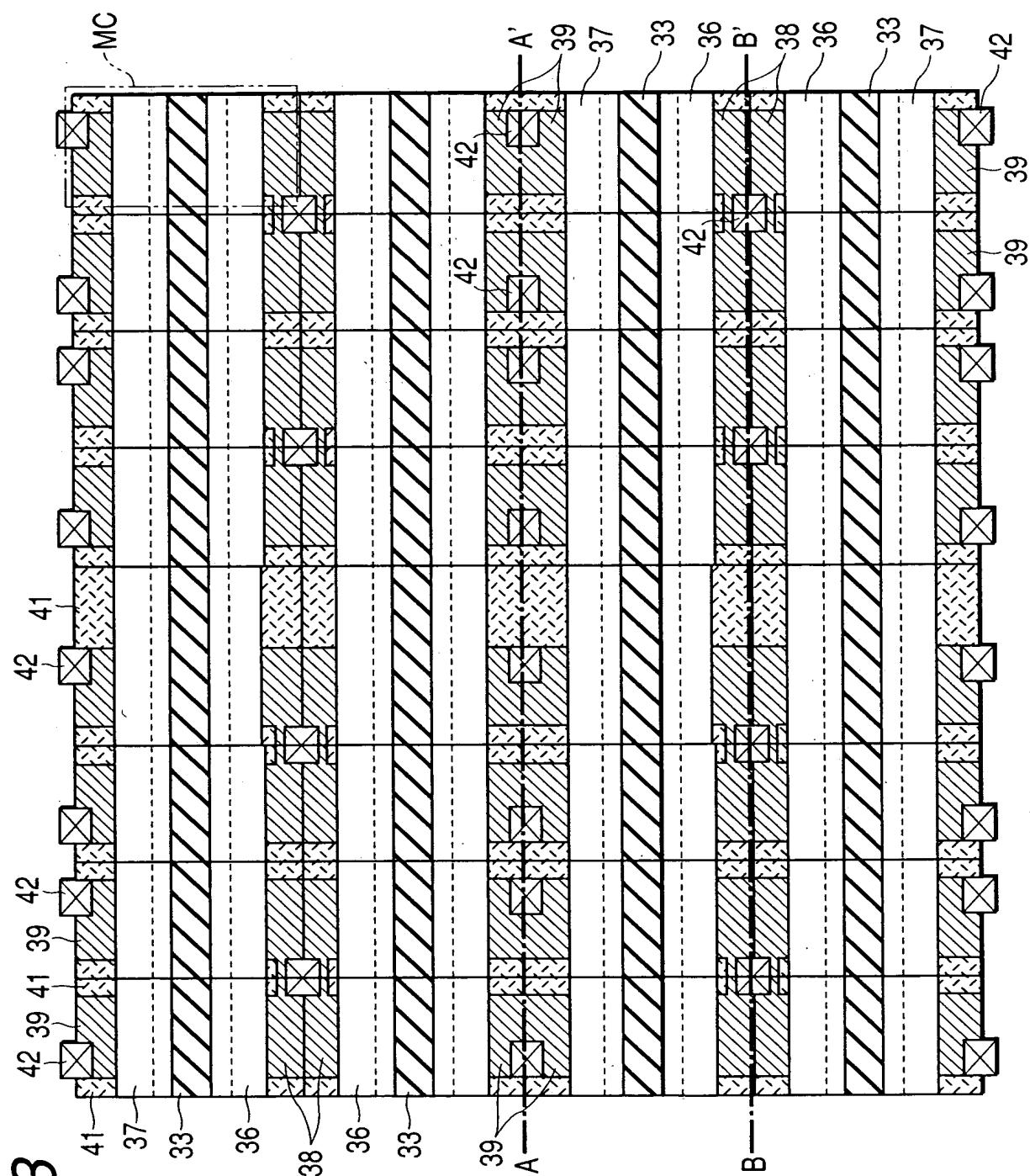


FIG. 3

FIG. 4

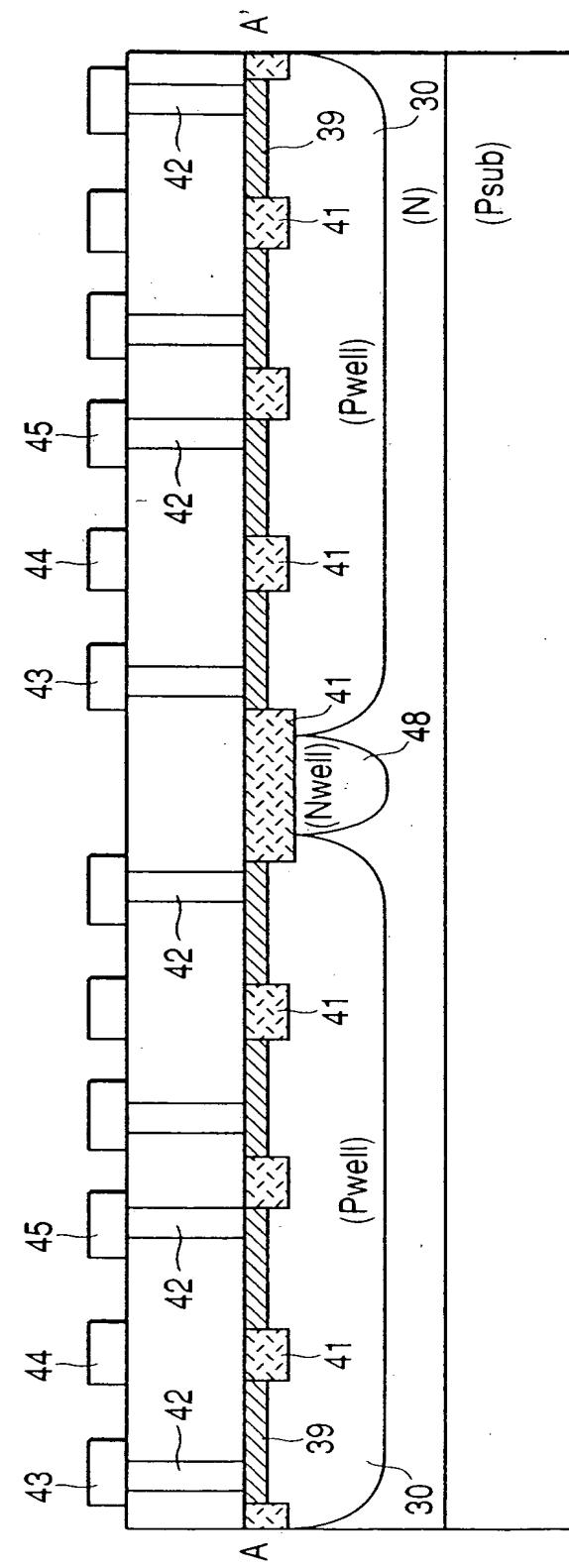


FIG. 5

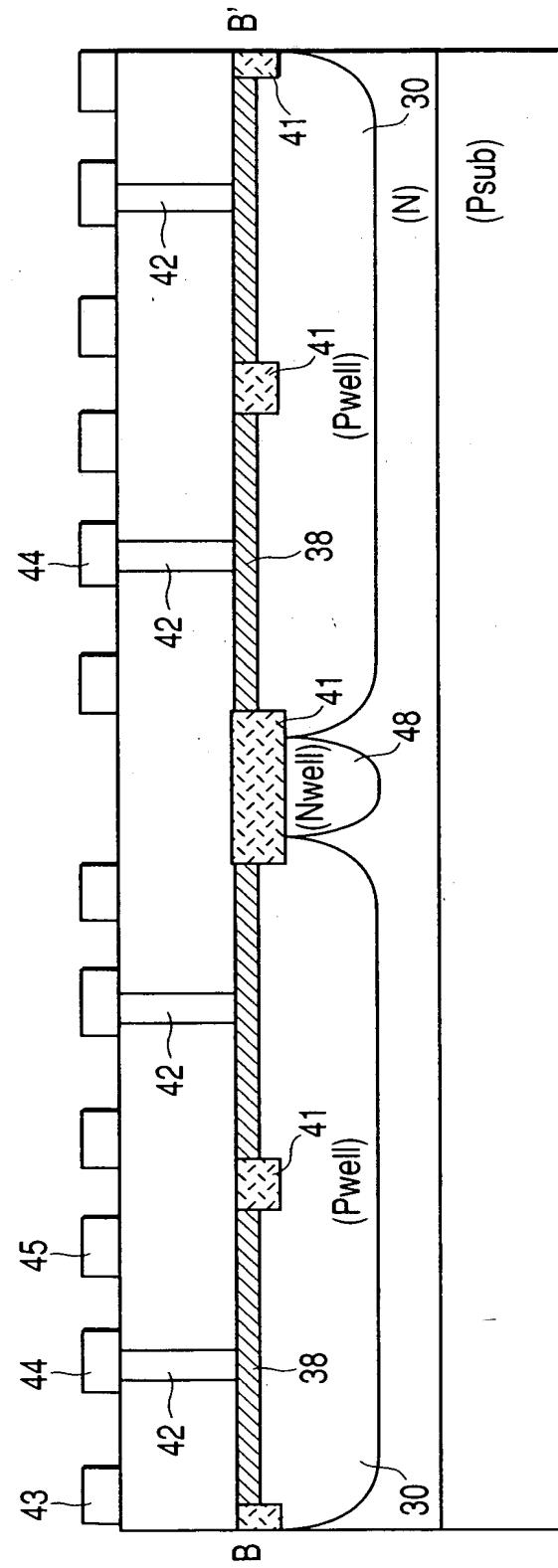


FIG. 6
ERASE (BYTE)

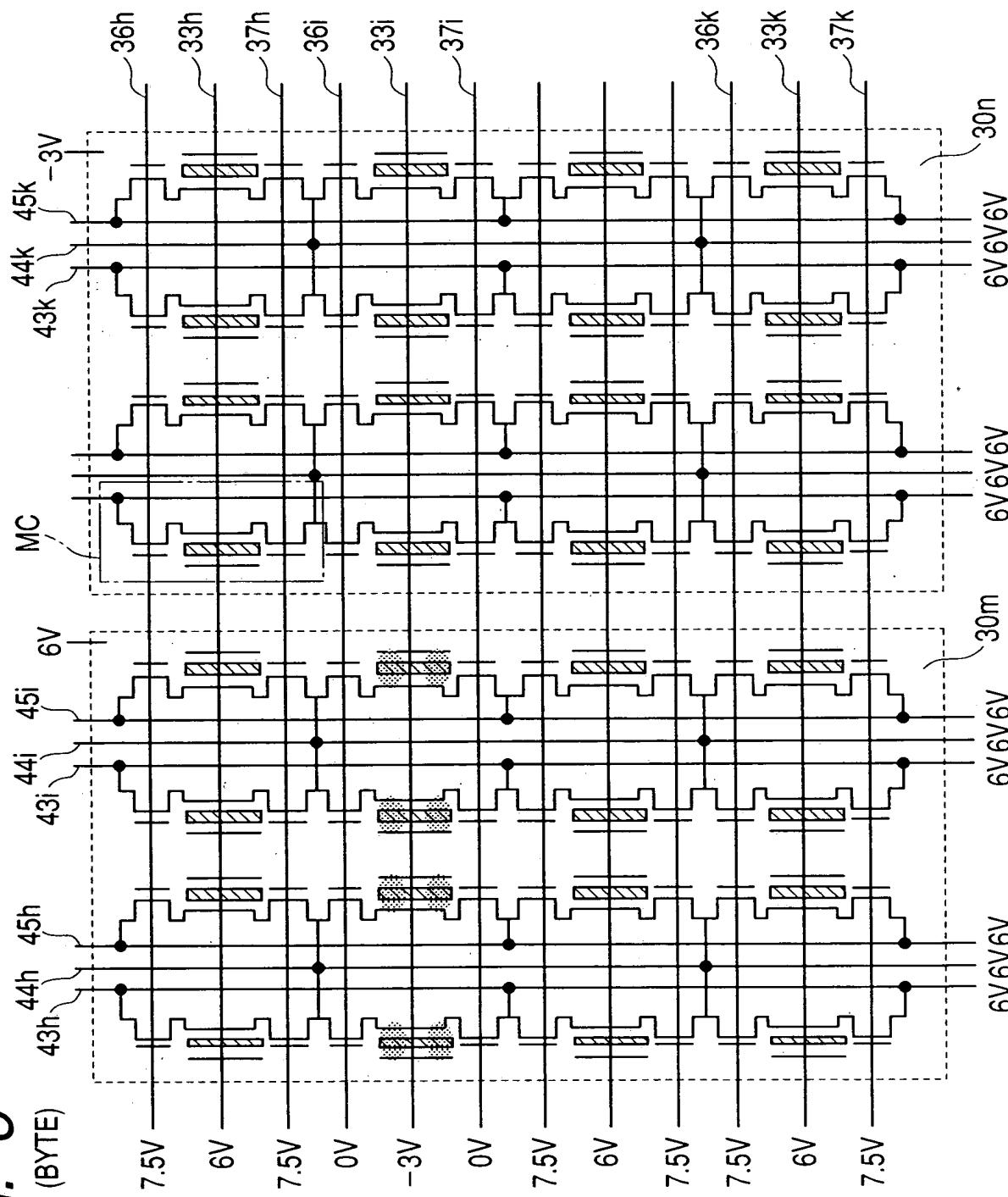


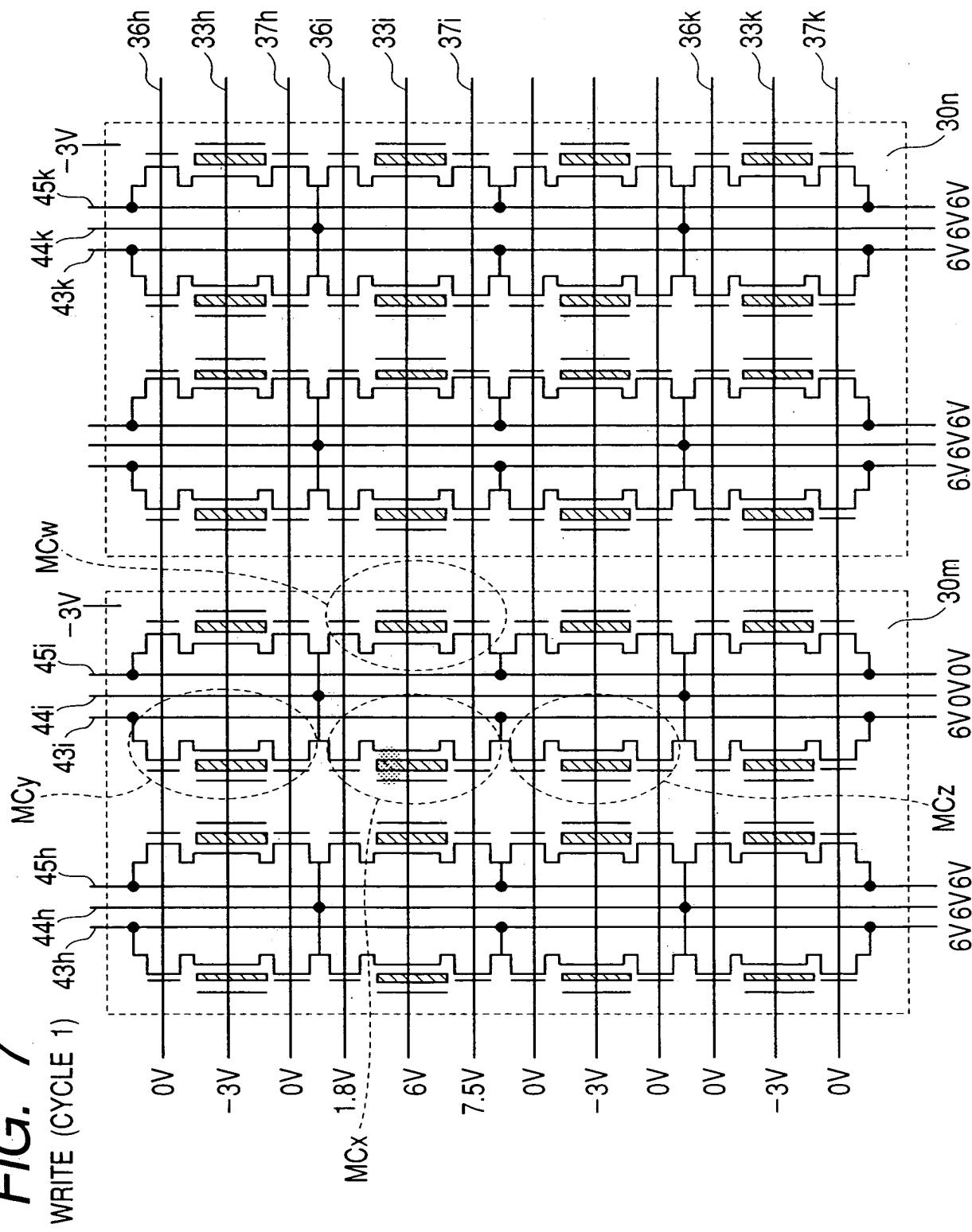
FIG. 7

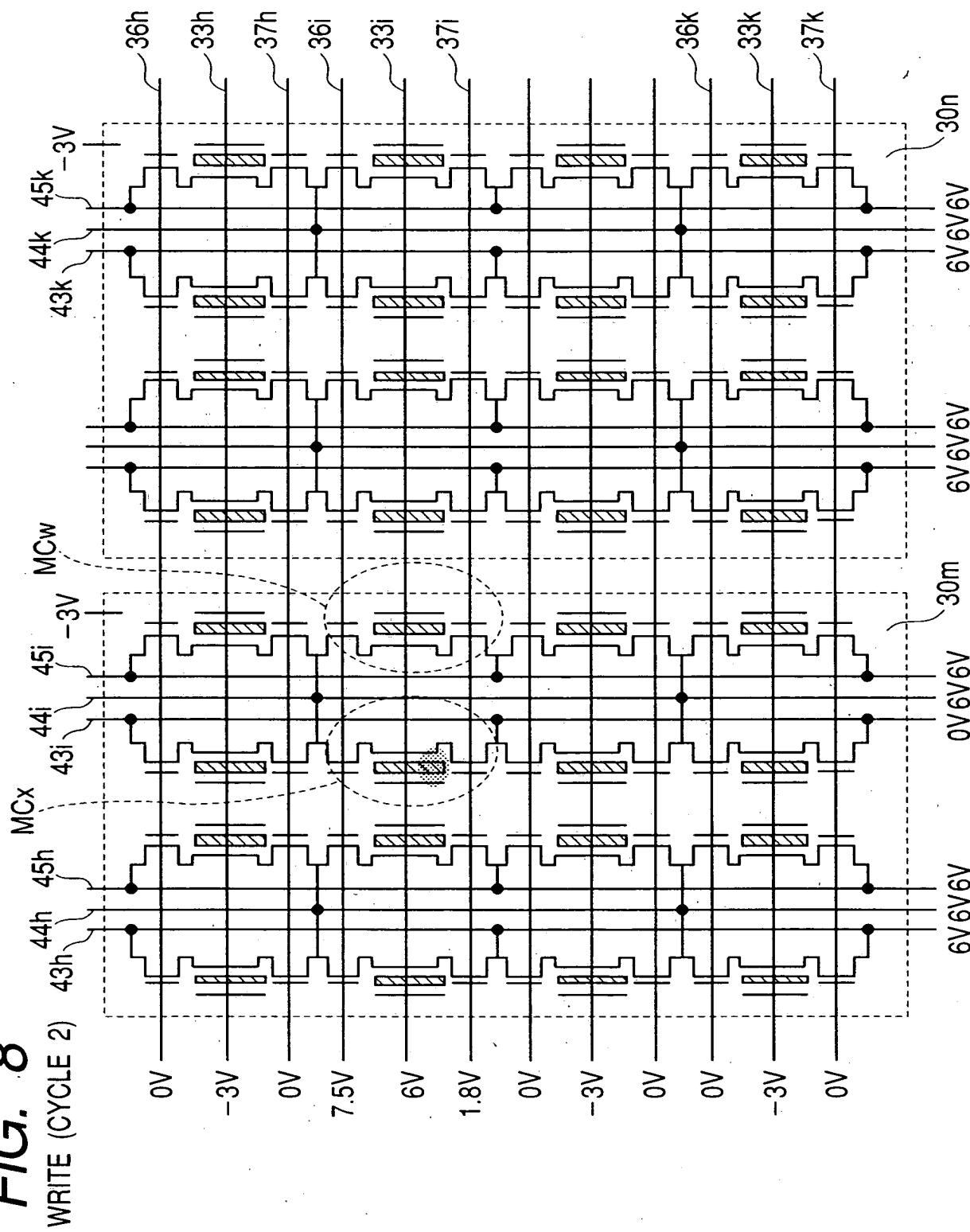
FIG. 8

FIG. 9

READ (CYCLE 1)

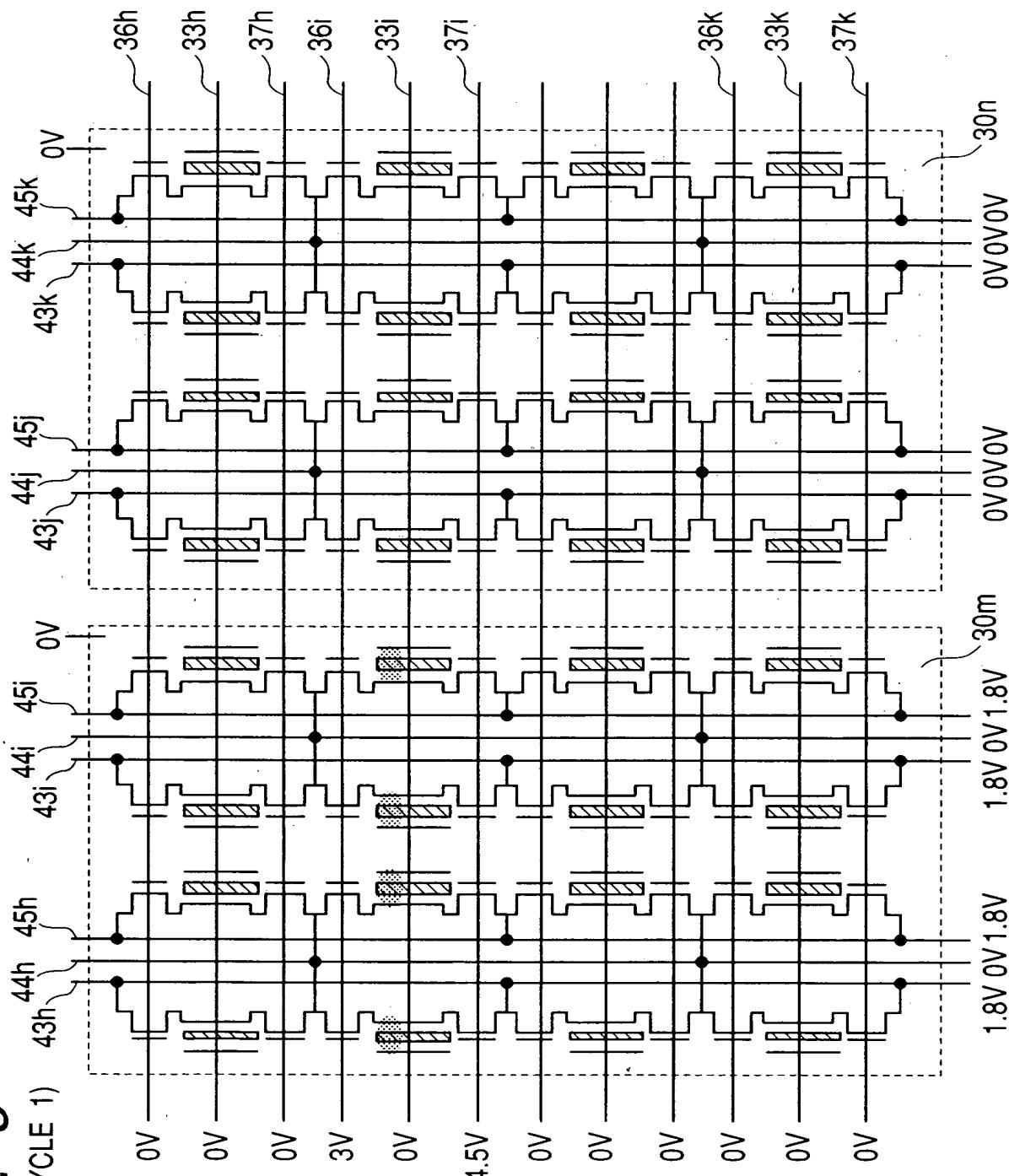


FIG. 10
READ (CYCLE 2)

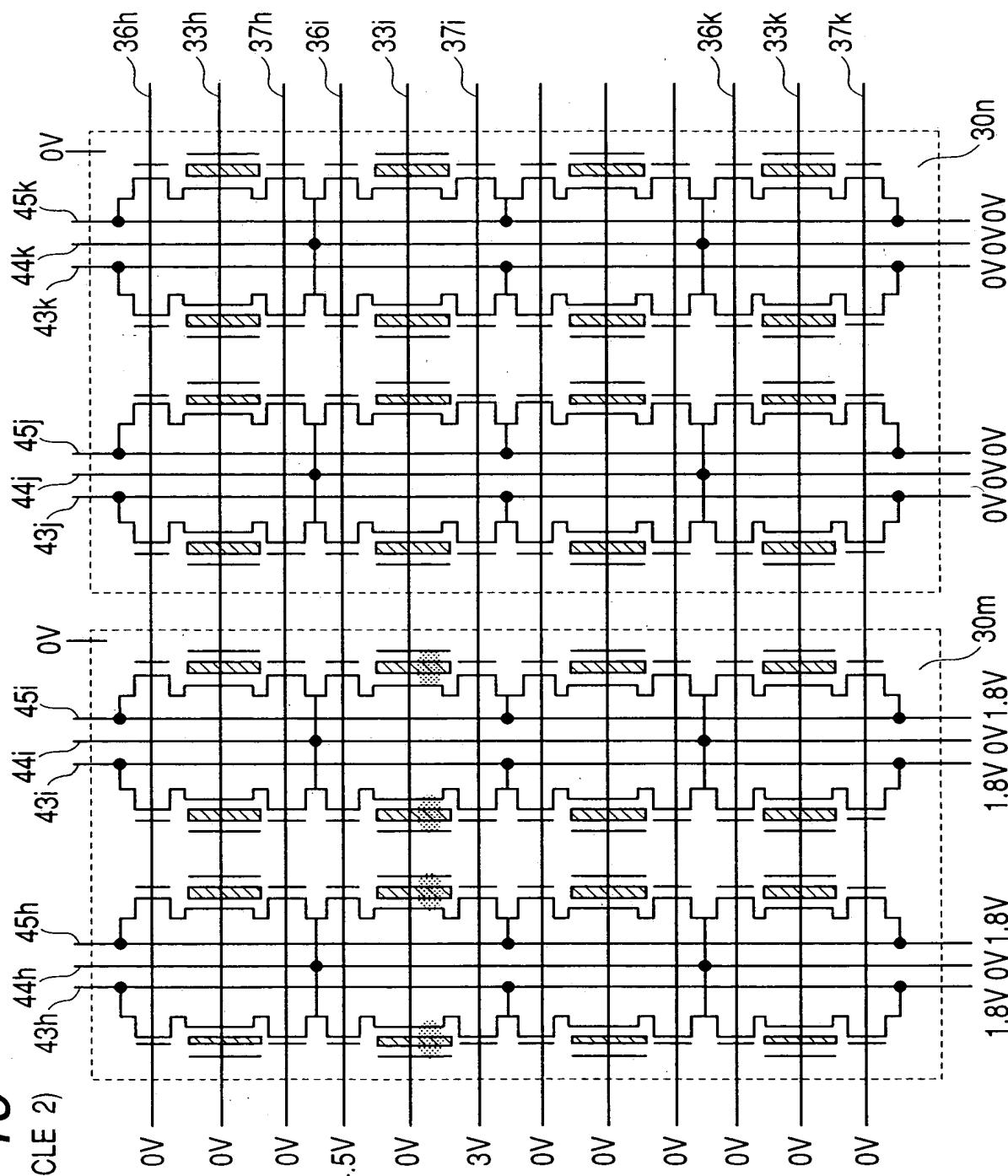


FIG. 11

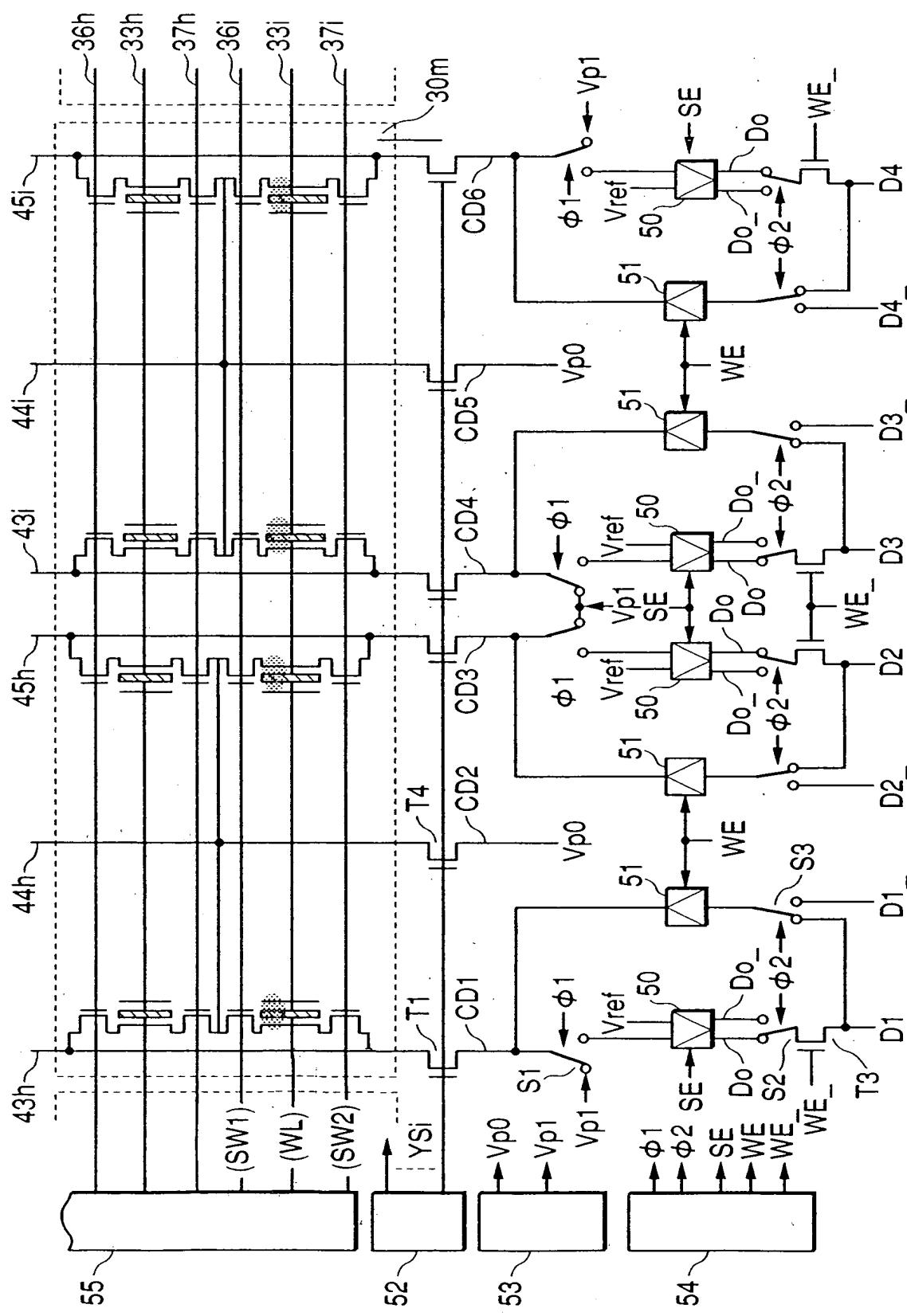


FIG. 12

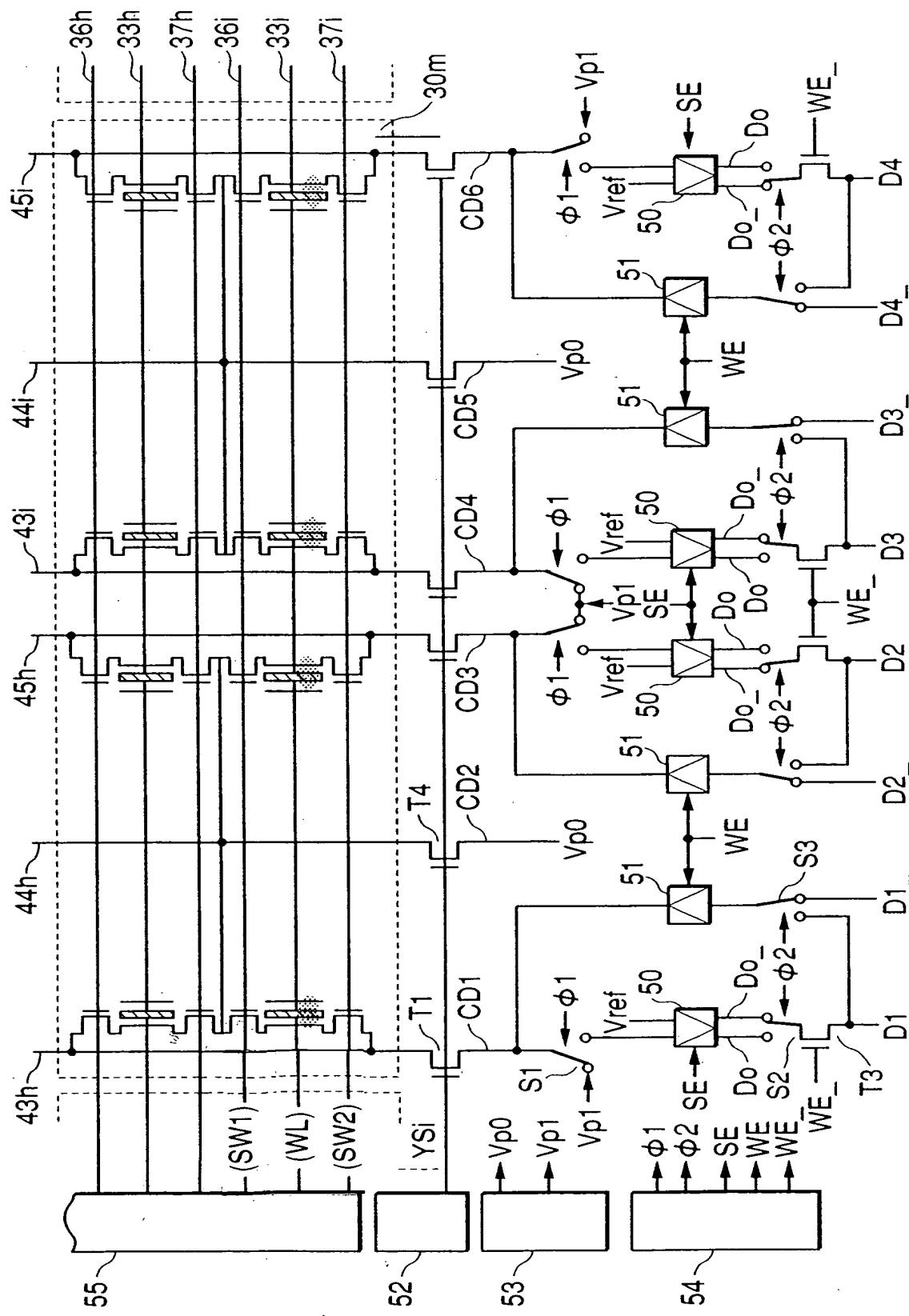


FIG. 13

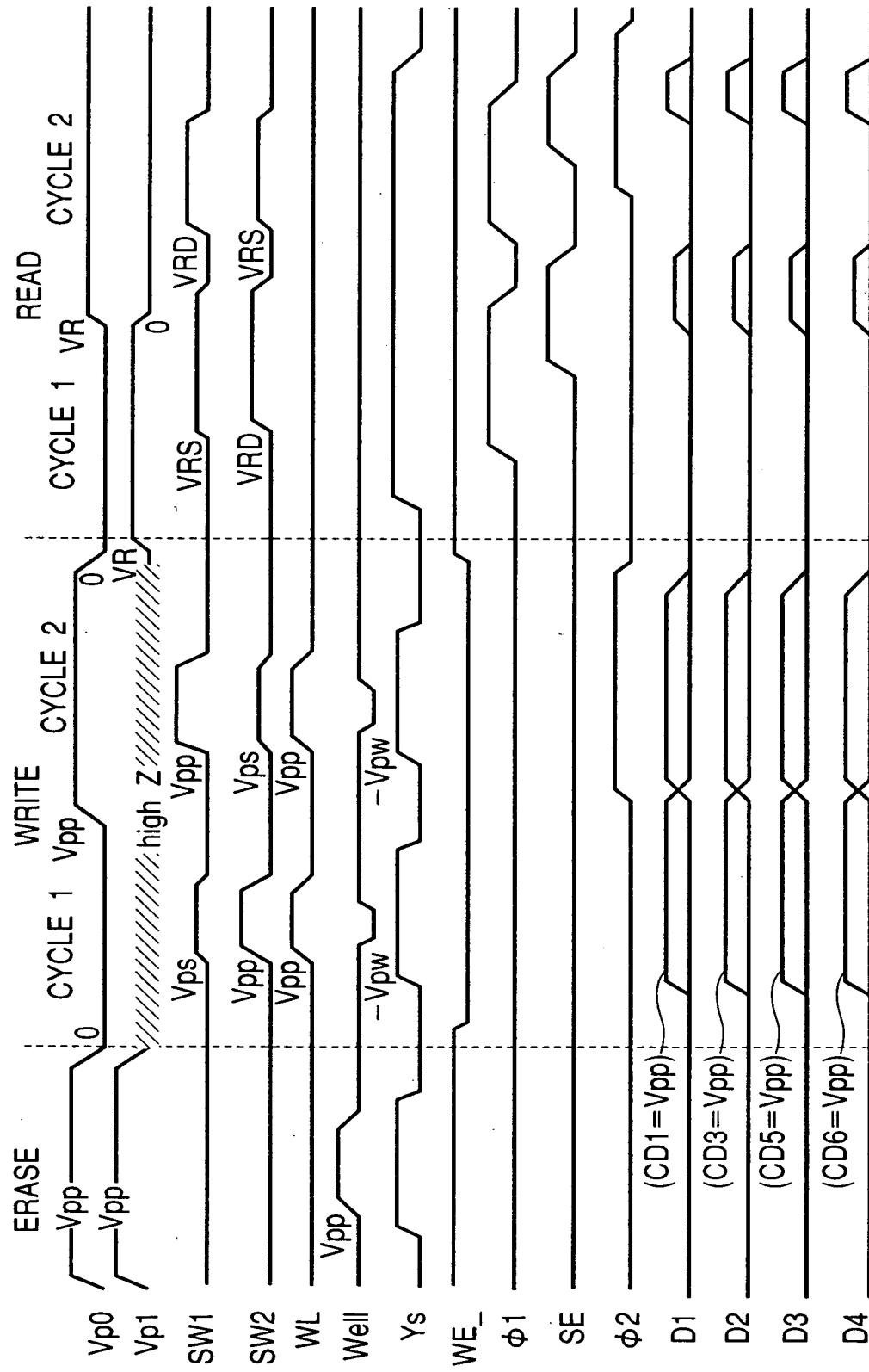


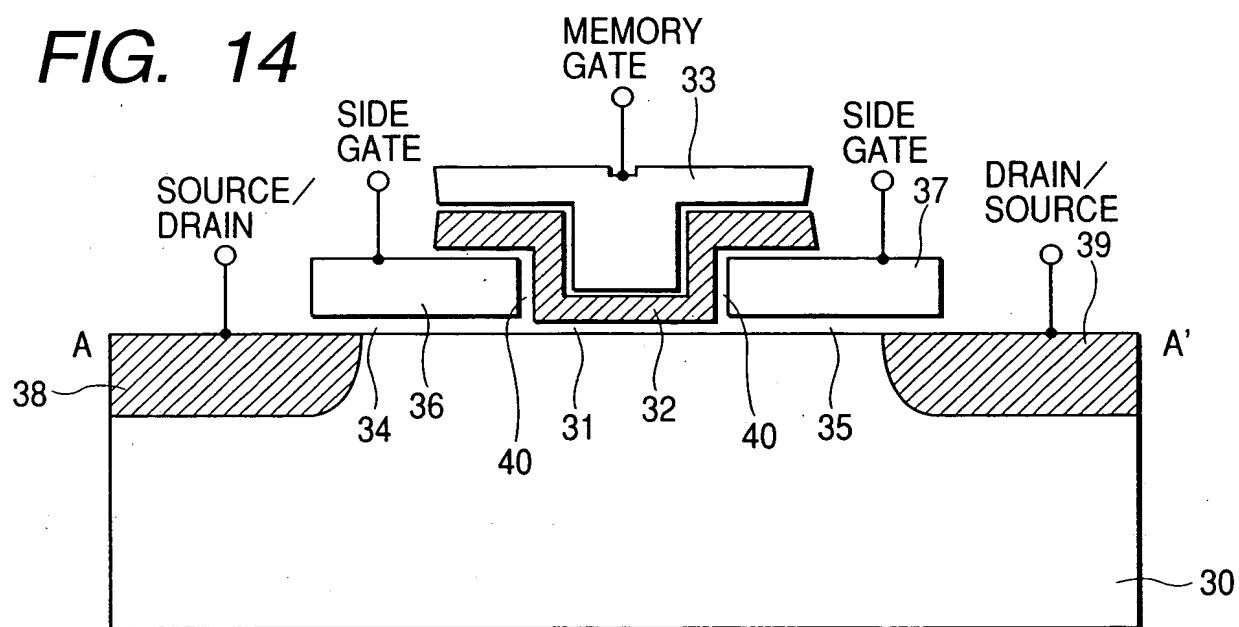
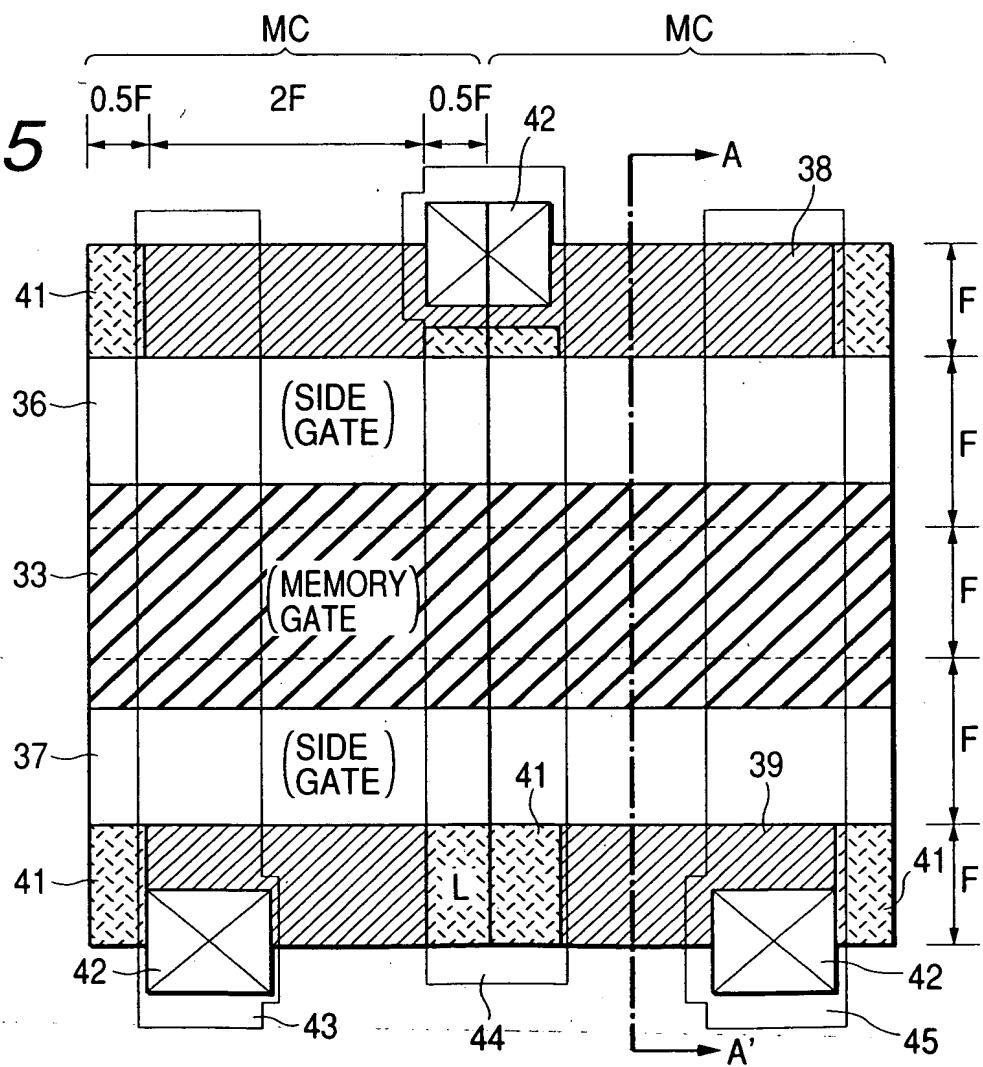
FIG. 14**FIG. 15**

FIG. 16

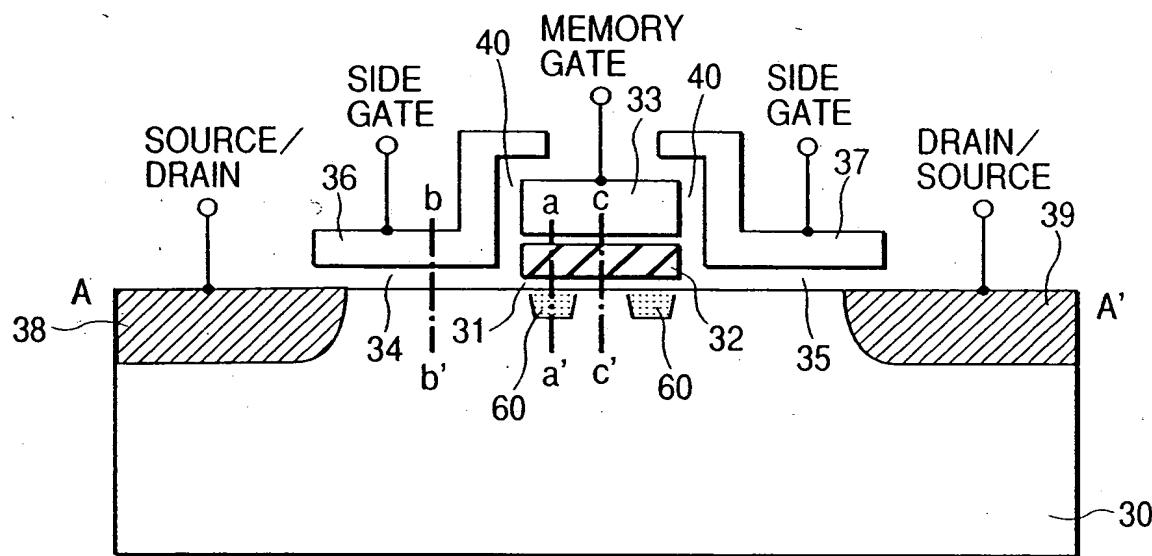
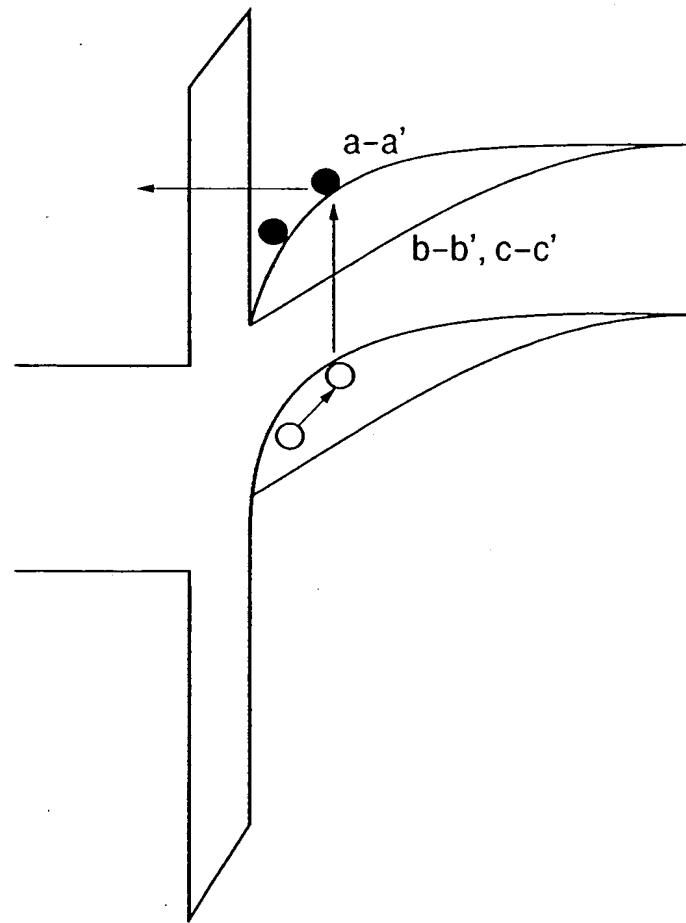


FIG. 17



(NITRIDE FILM) (OXIDE FILM) (SUBSTRATE)

FIG. 18

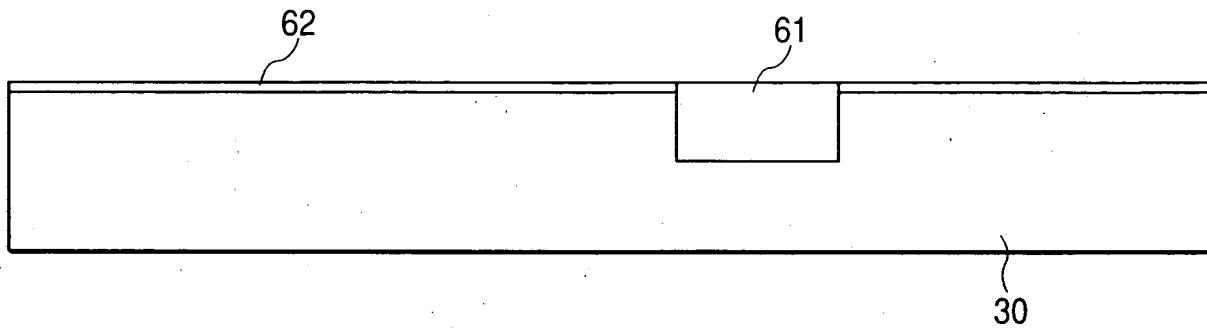


FIG. 19

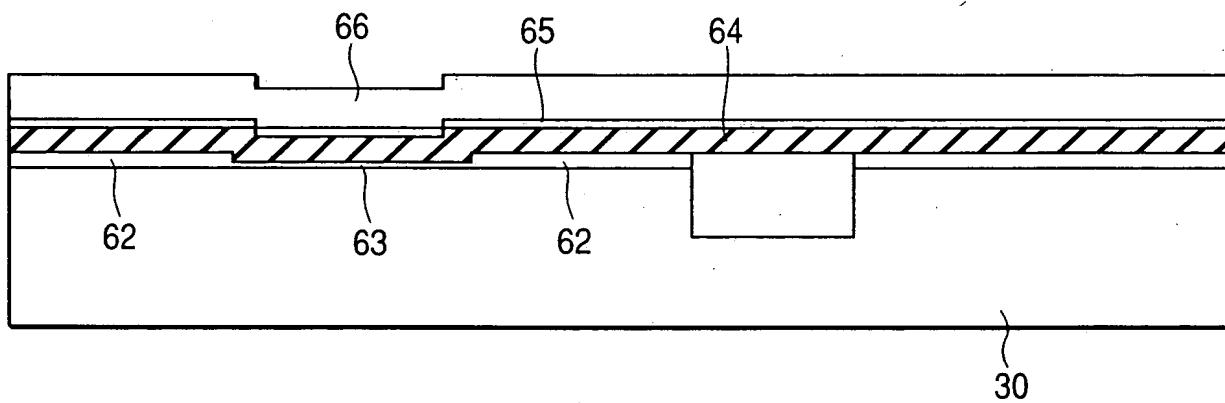


FIG. 20

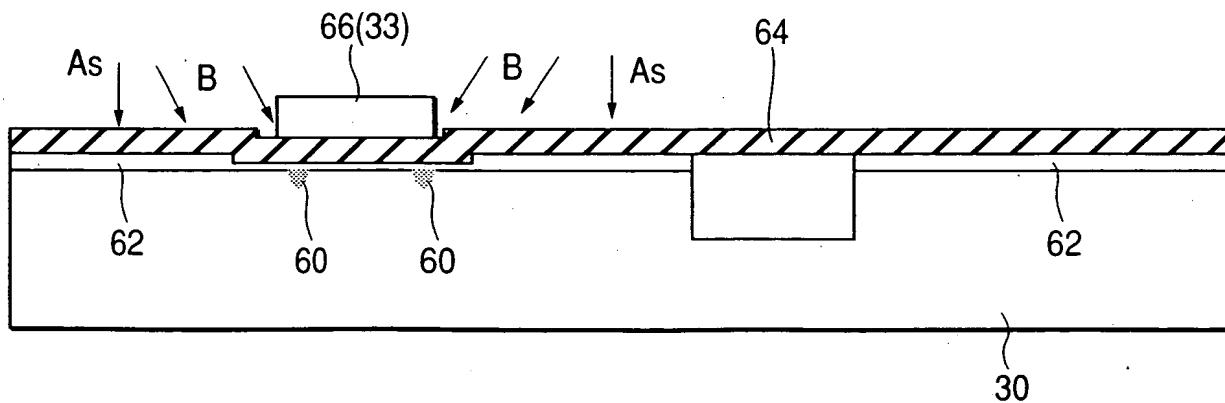


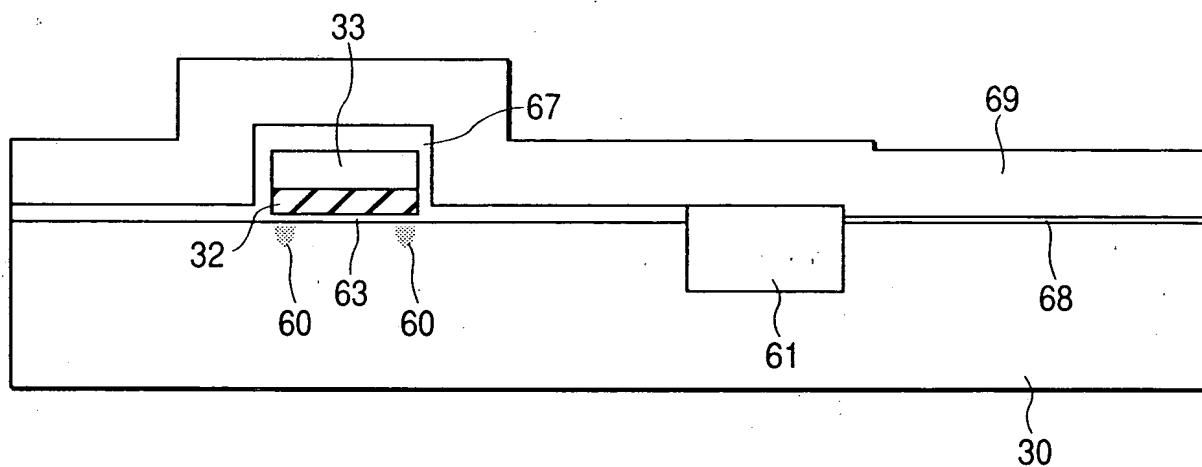
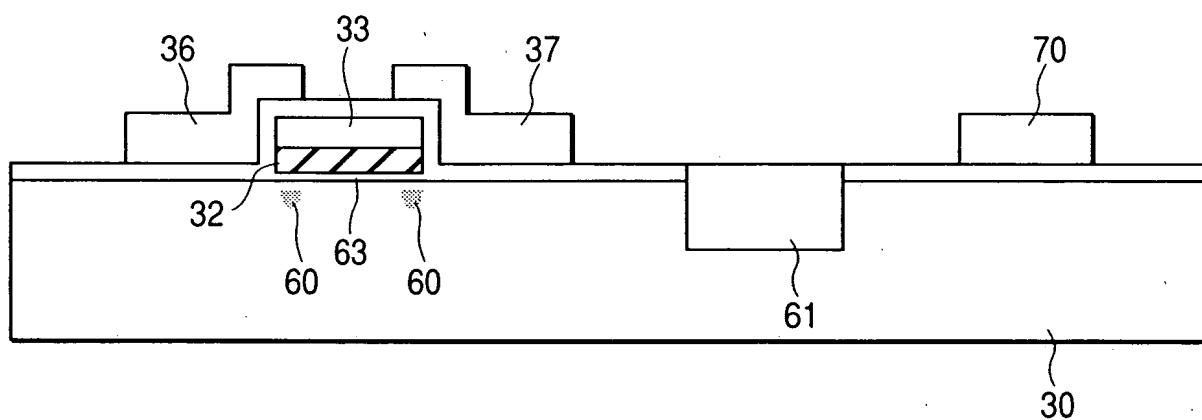
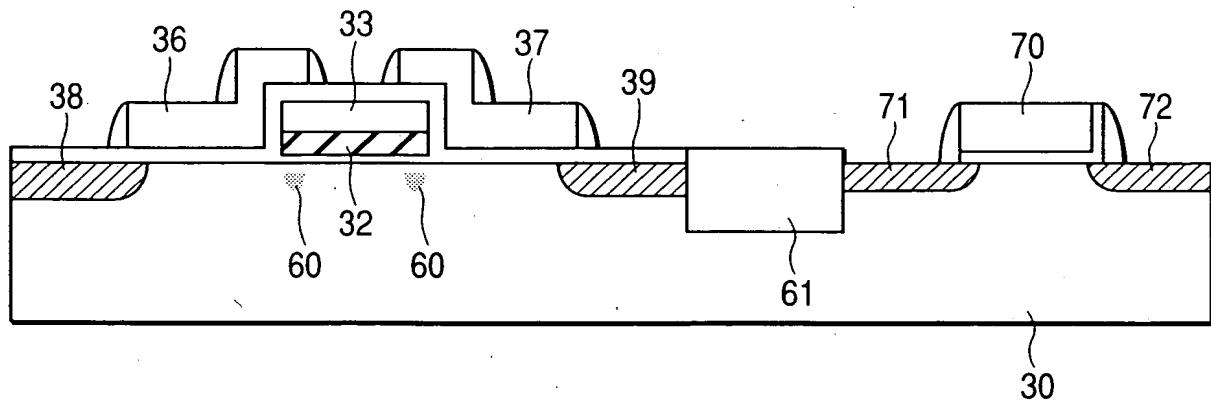
FIG. 21*FIG. 22**FIG. 23*

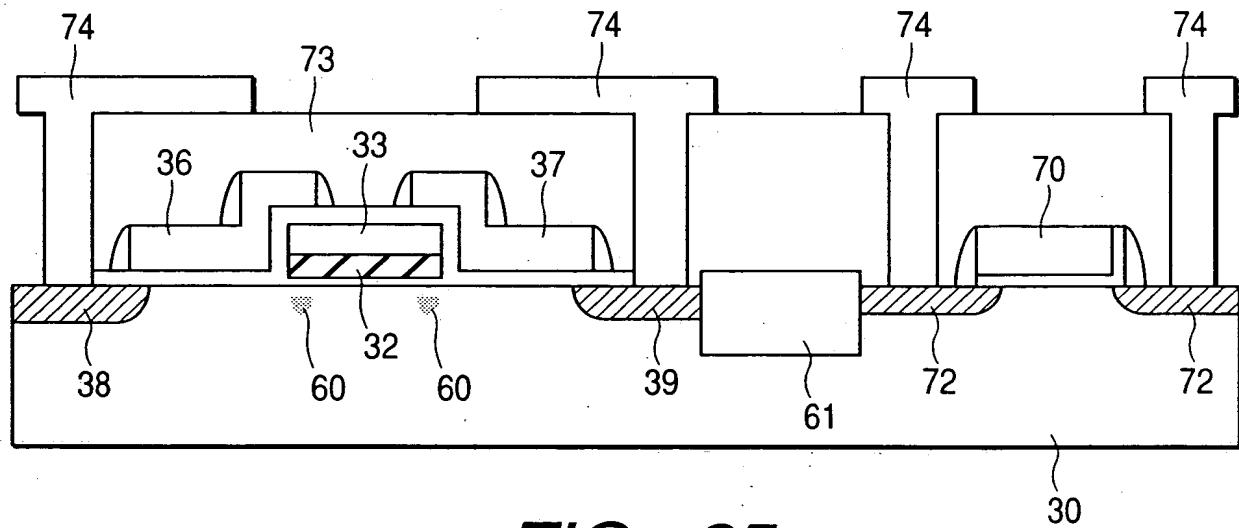
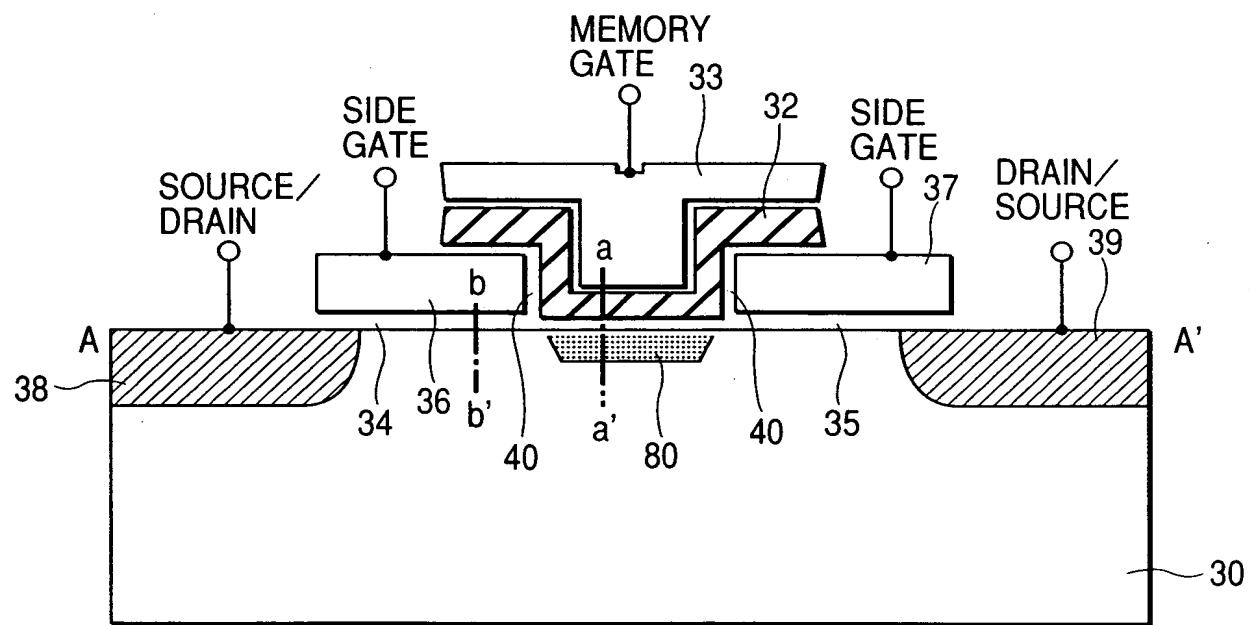
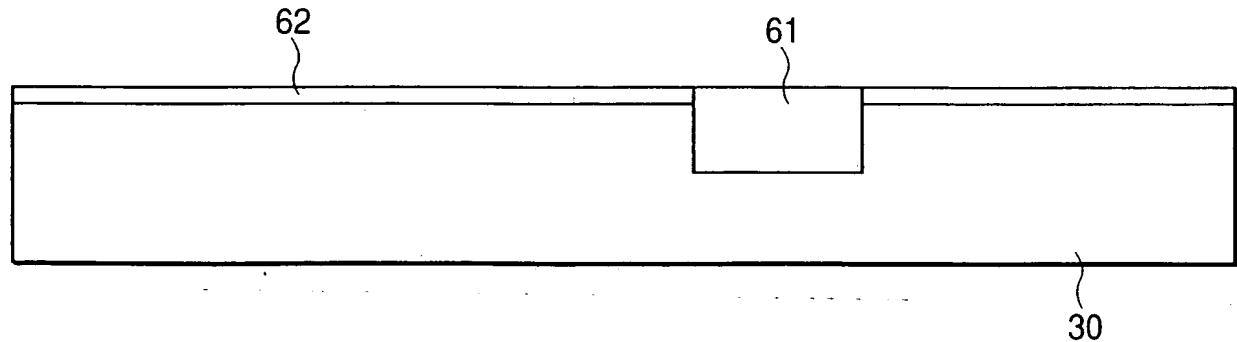
FIG. 24**FIG. 25****FIG. 26**

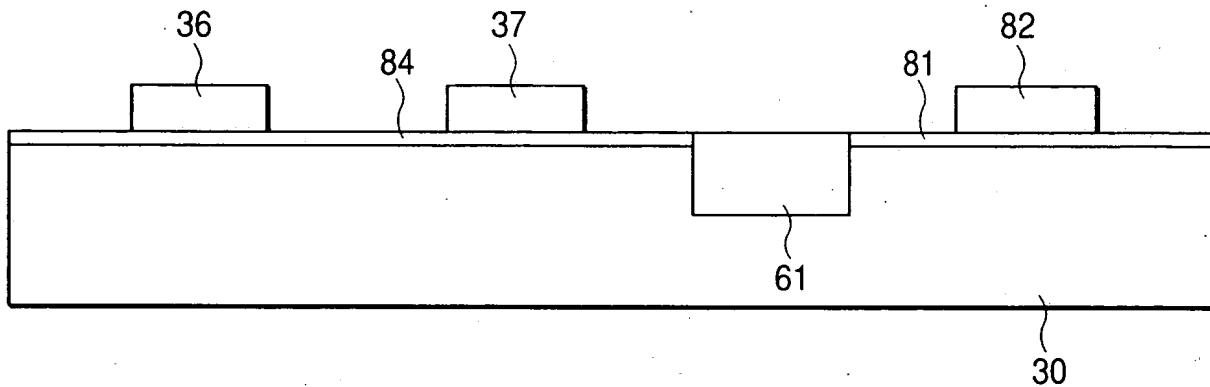
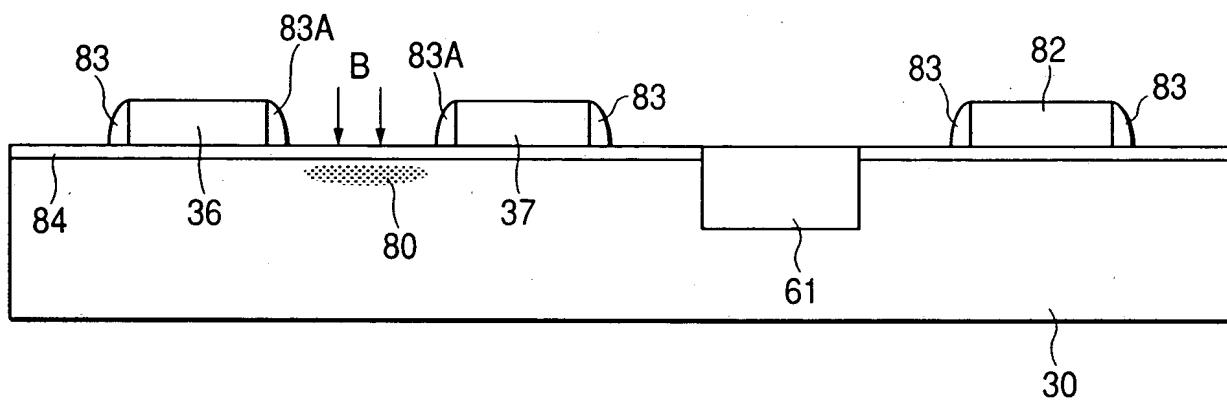
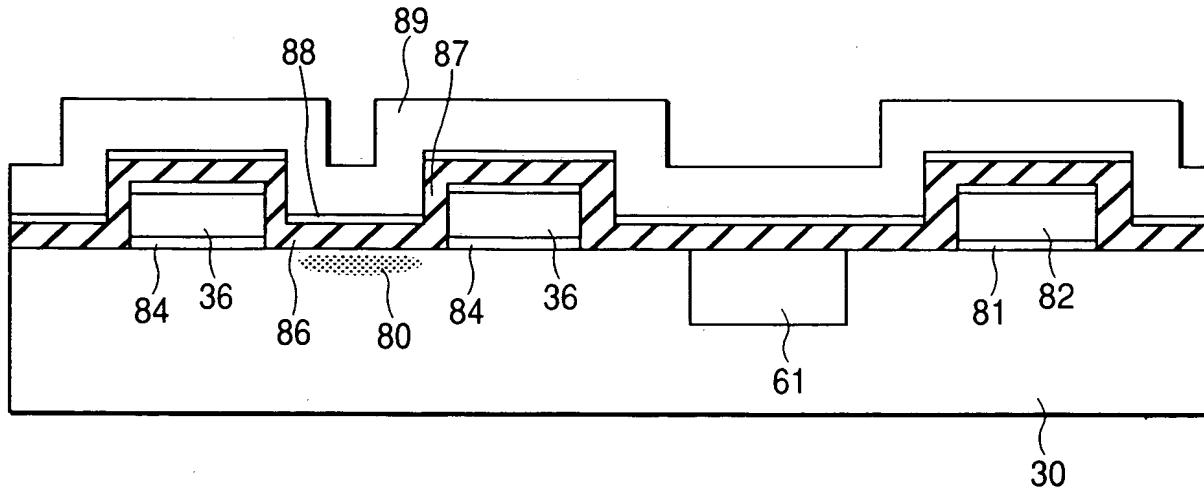
FIG. 27***FIG. 28******FIG. 29***

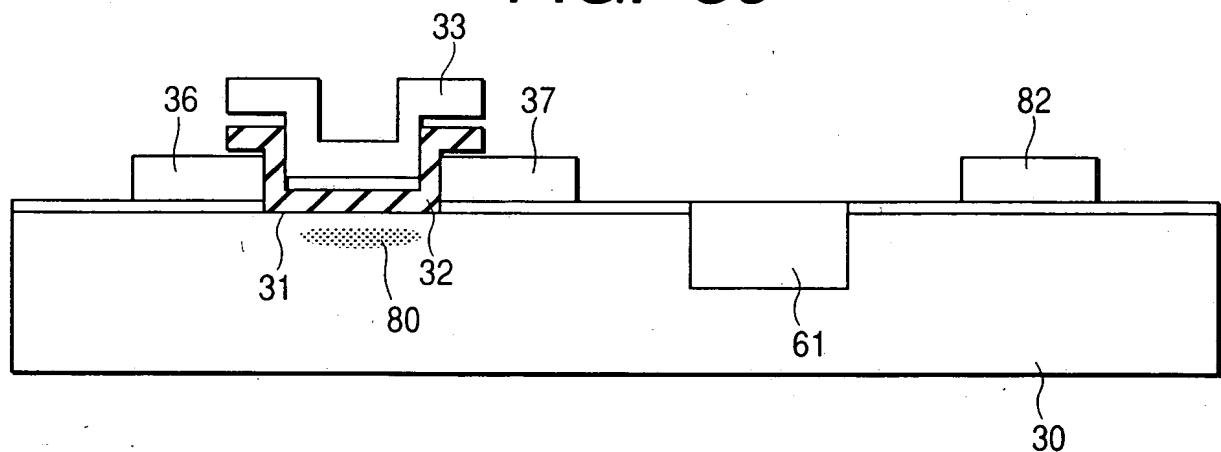
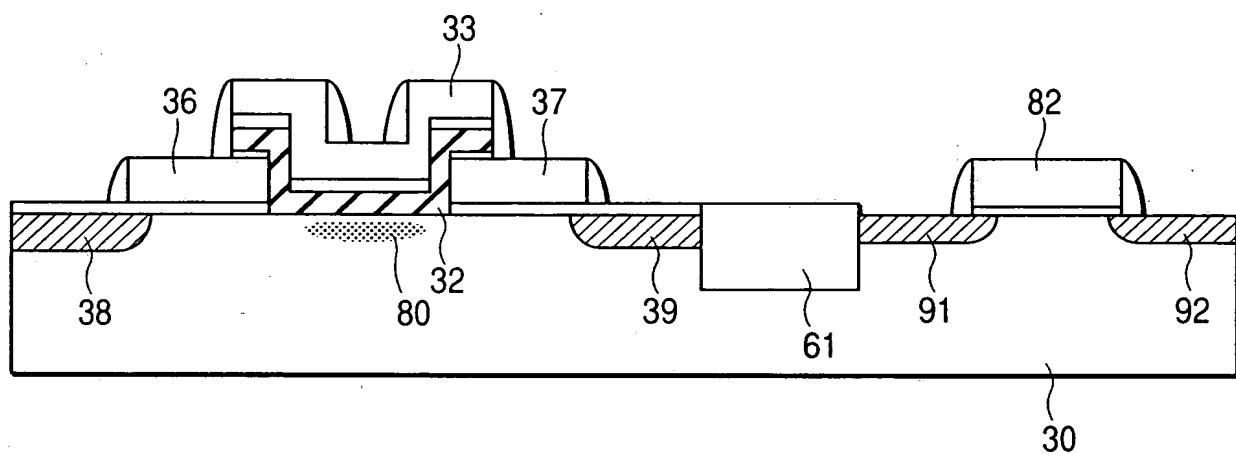
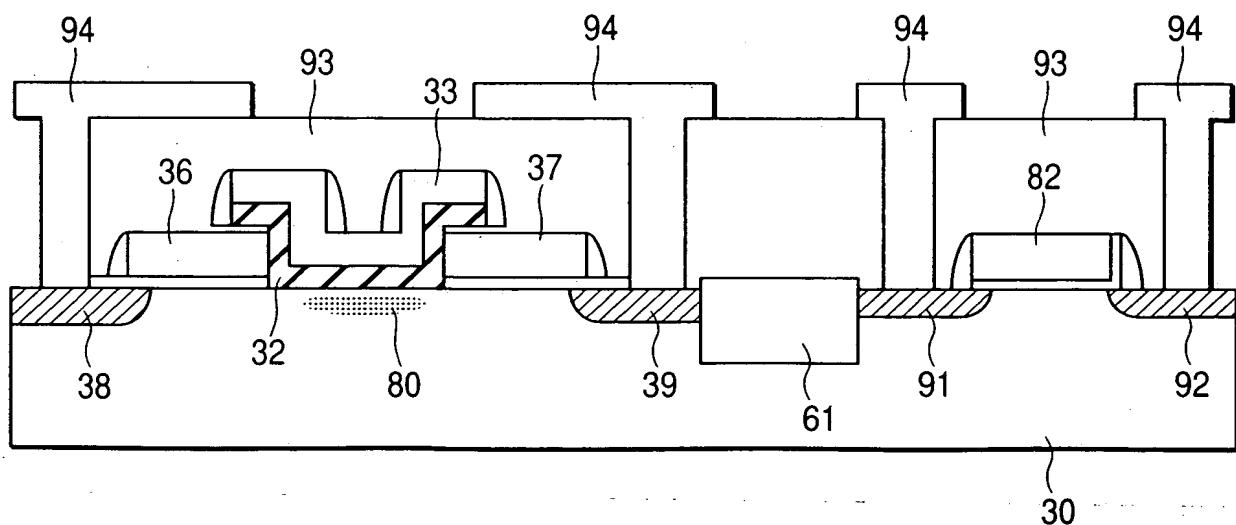
FIG. 30***FIG. 31******FIG. 32***

FIG. 33
ERASE (2 BITS)

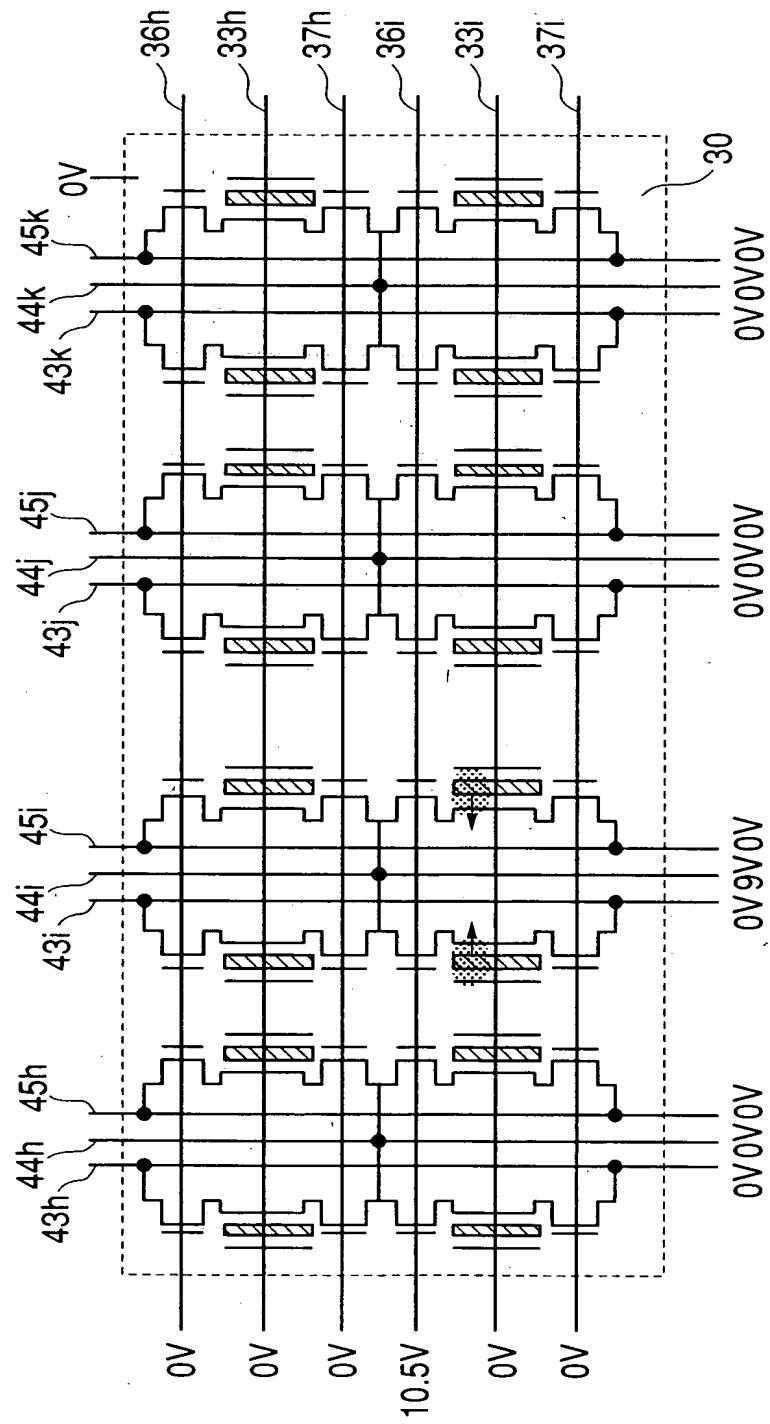


FIG. 34
ERASE (4 BITS)

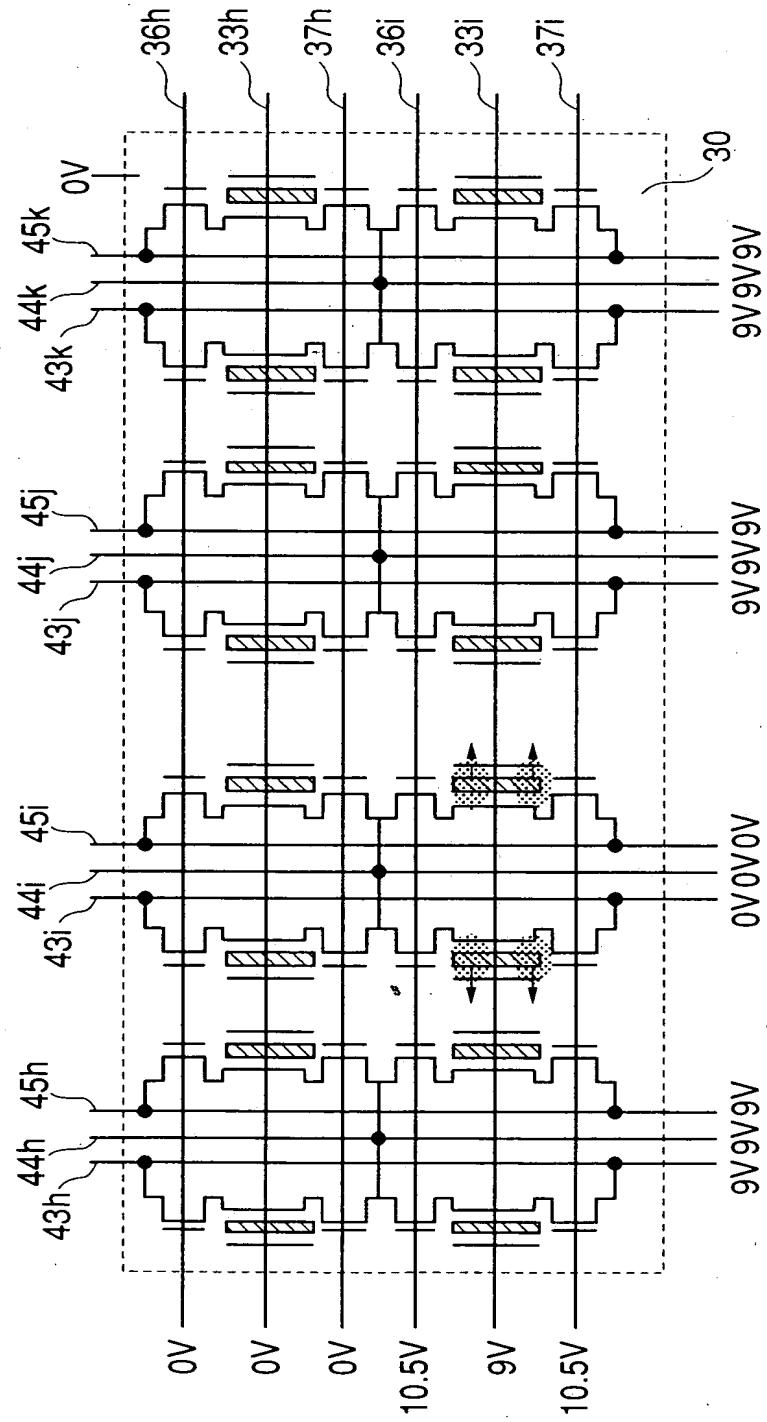


FIG. 35

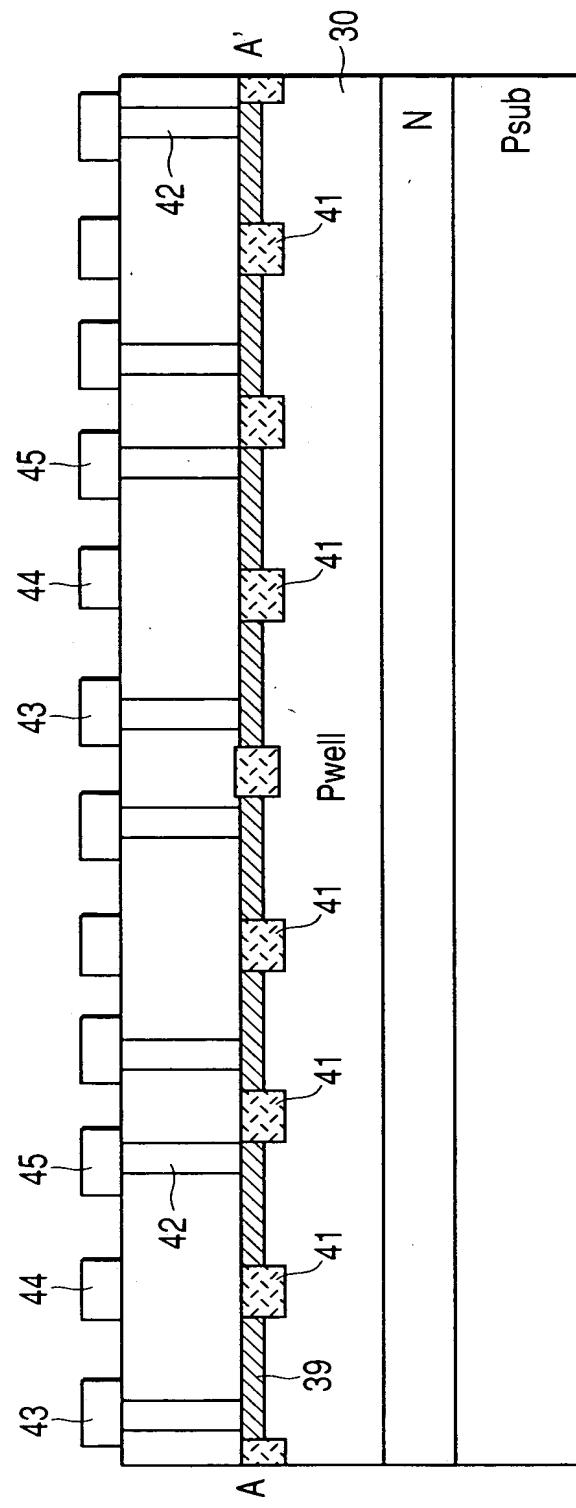
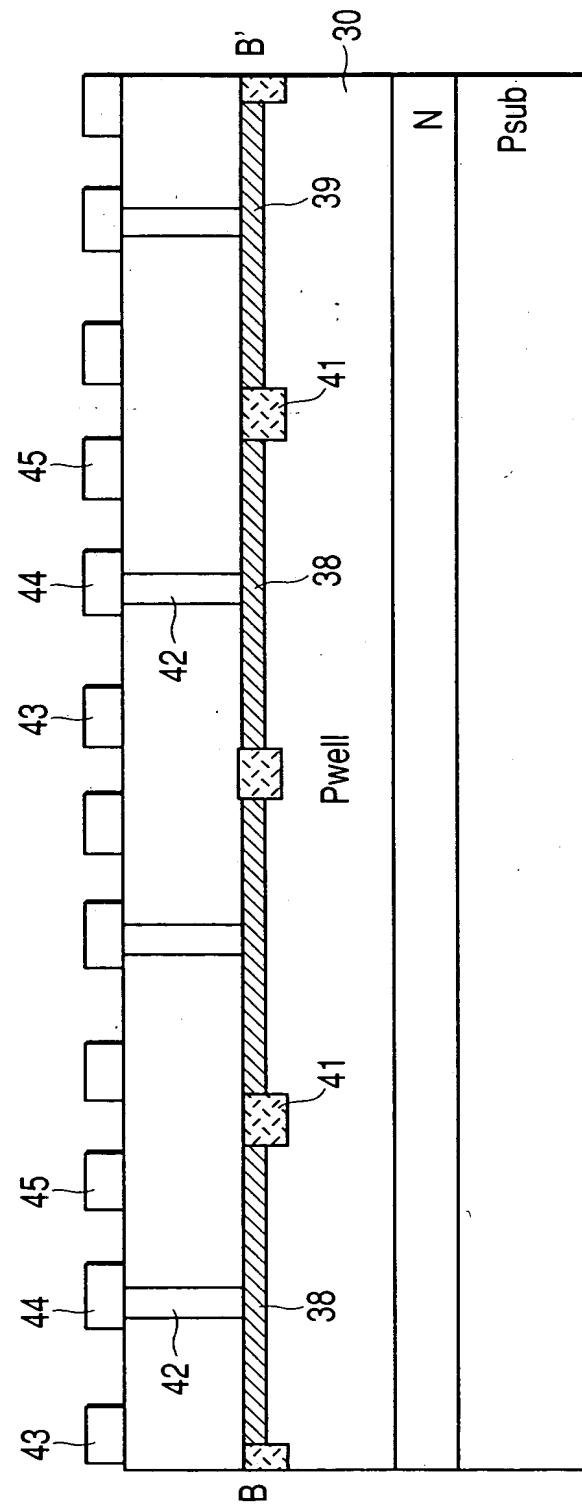


FIG. 36



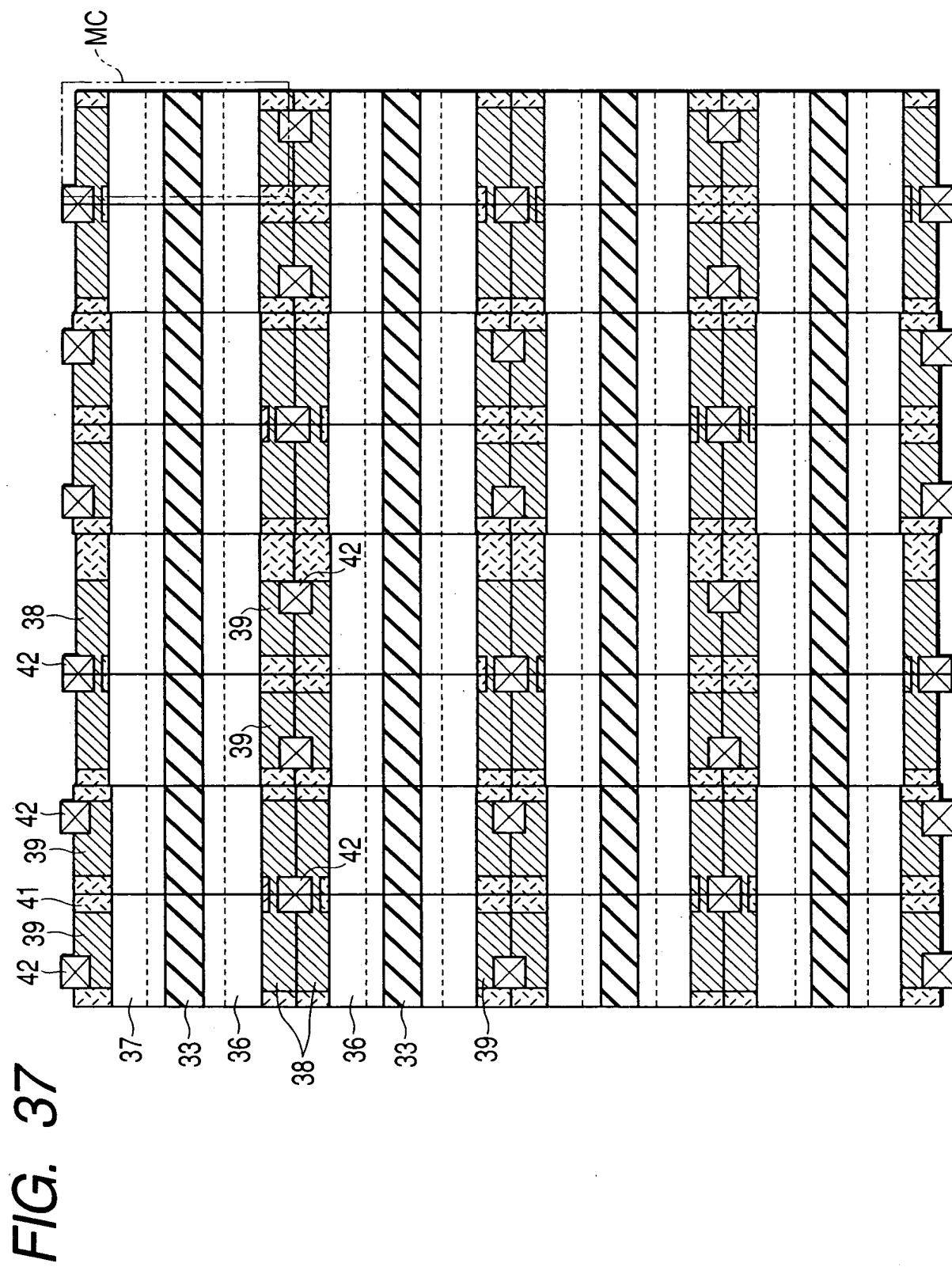


FIG. 38

26 / 34

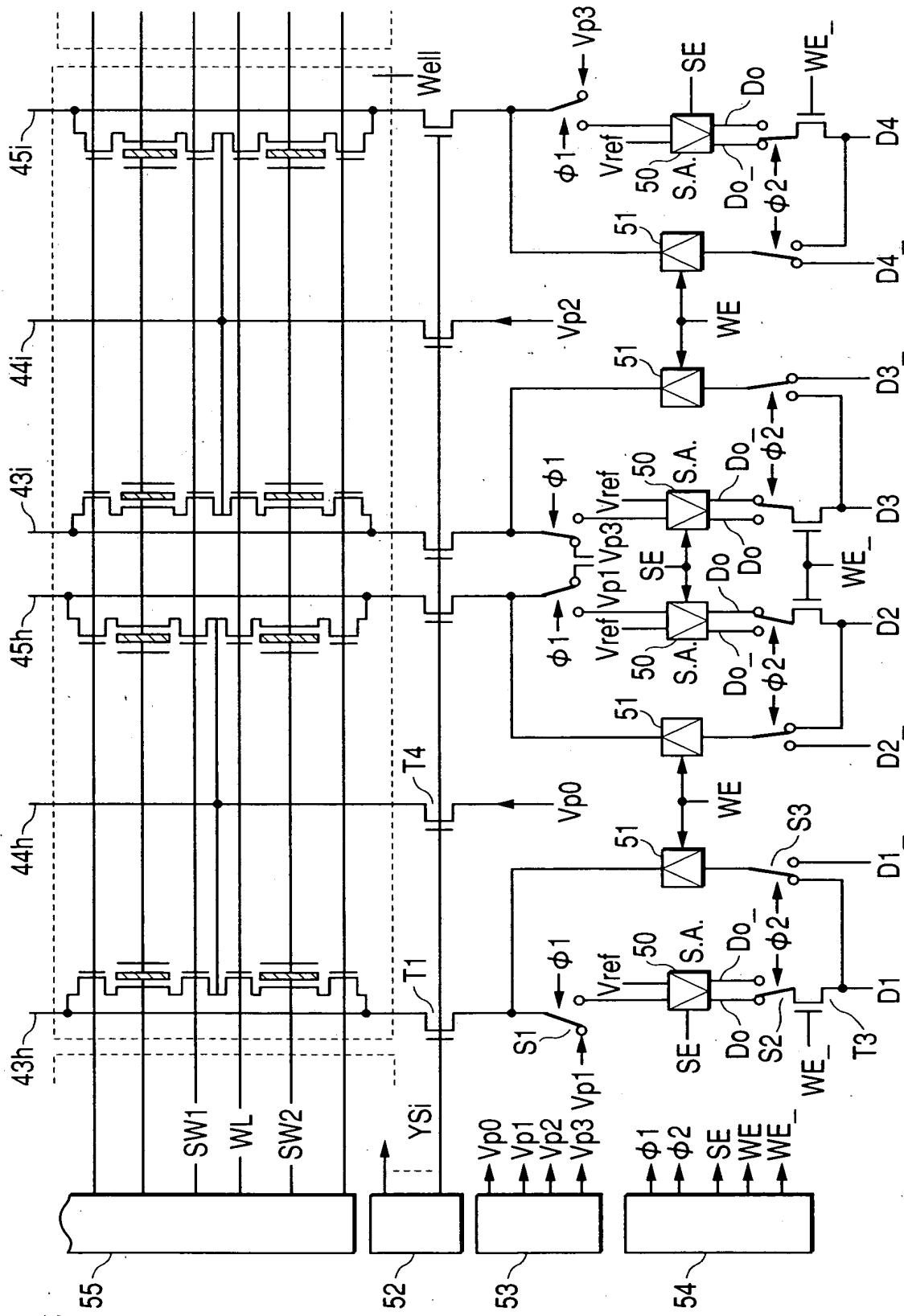


FIG. 39

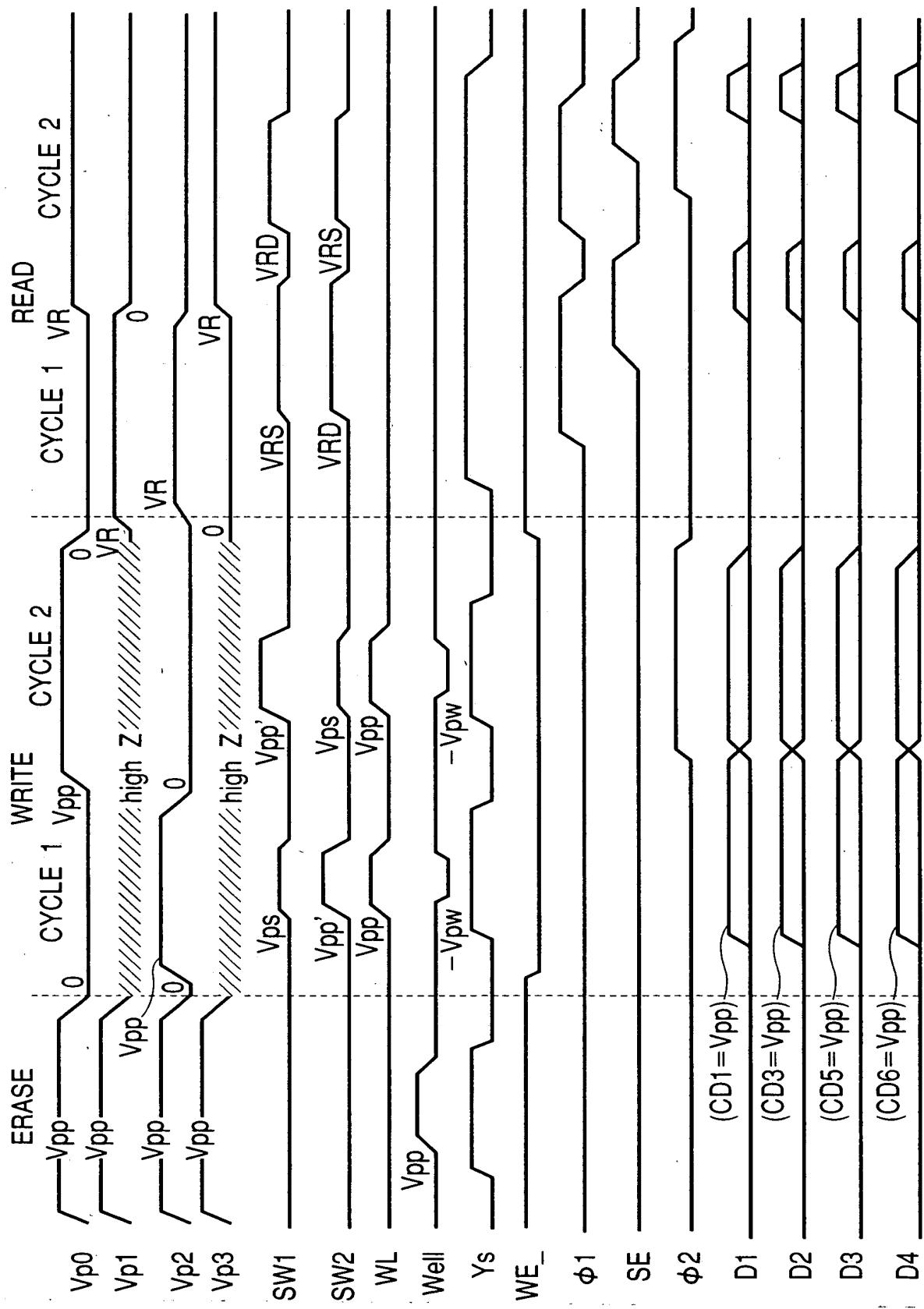


FIG. 40

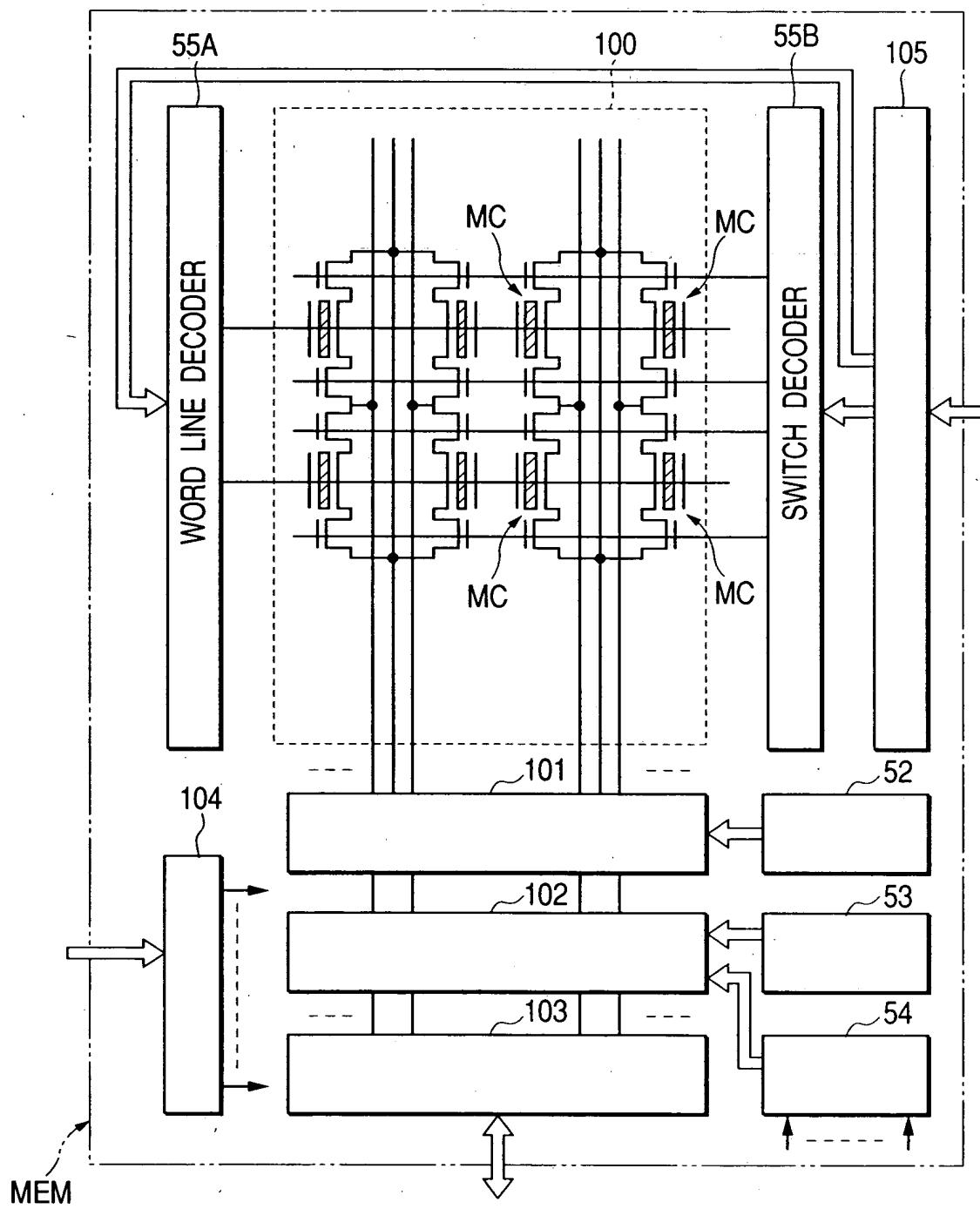


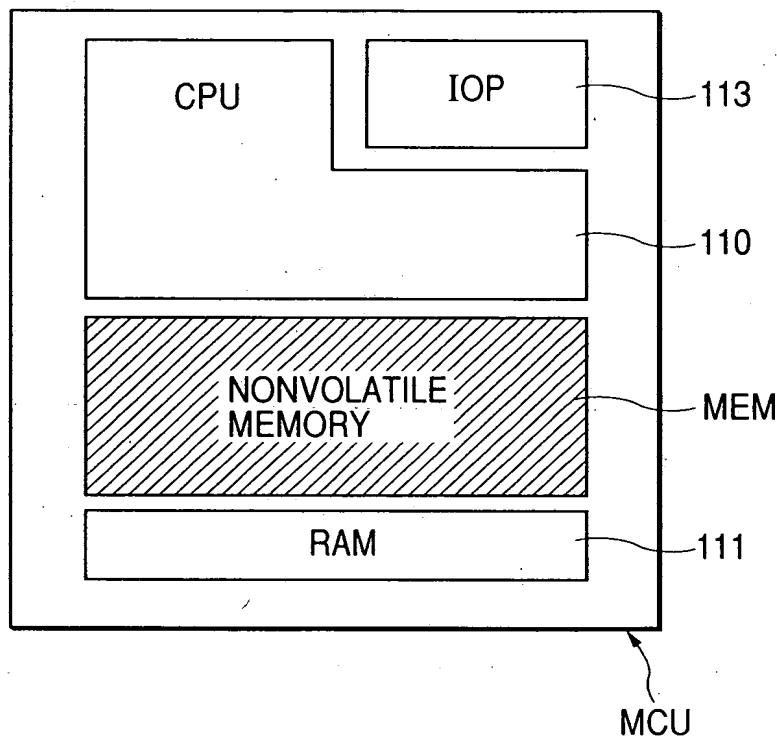
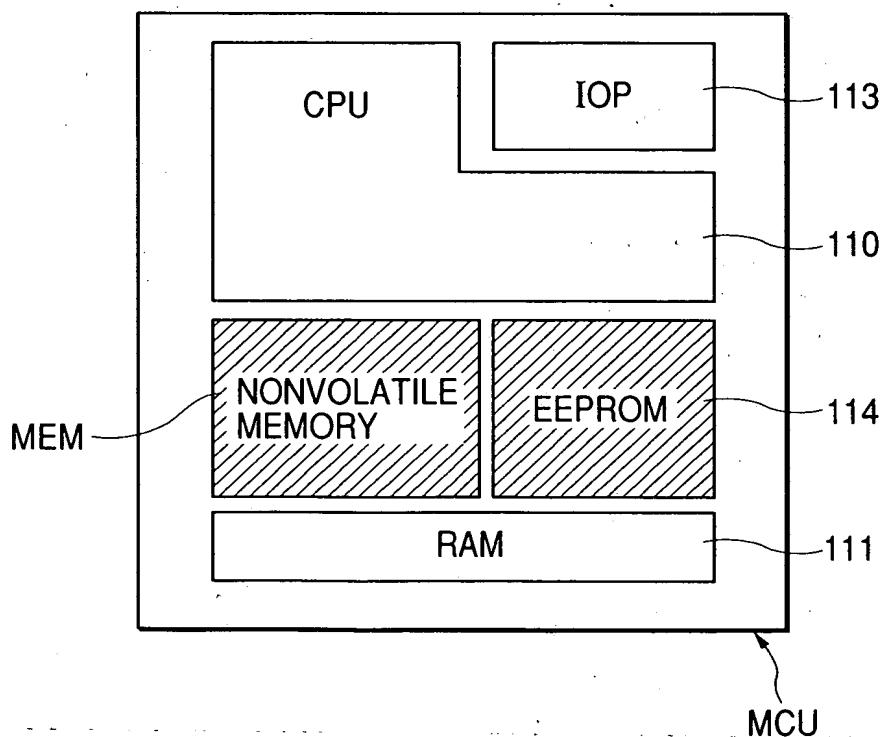
FIG. 41***FIG. 42***

FIG. 43

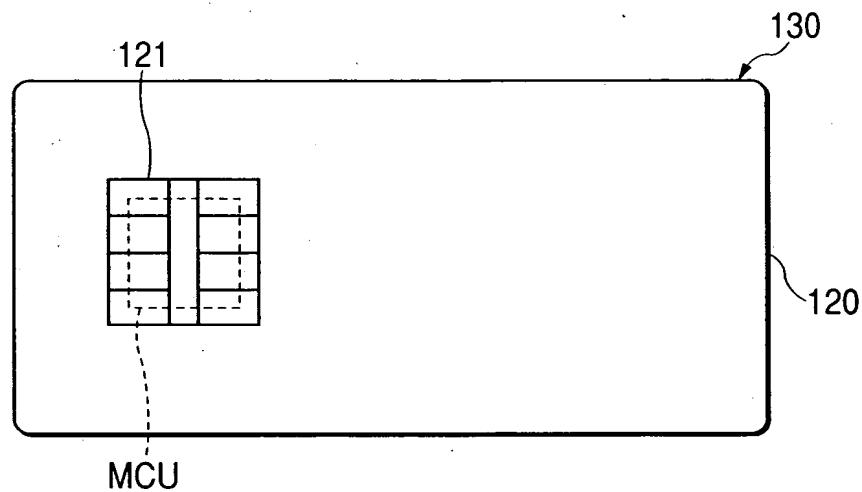


FIG. 44

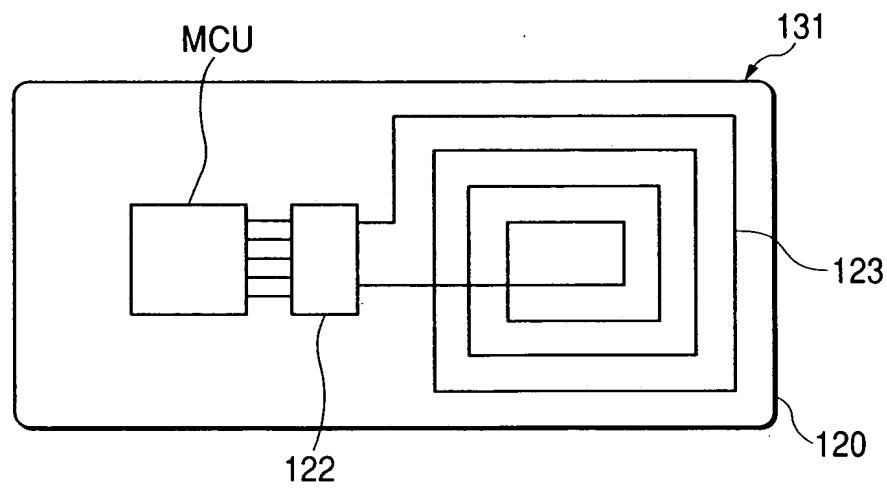


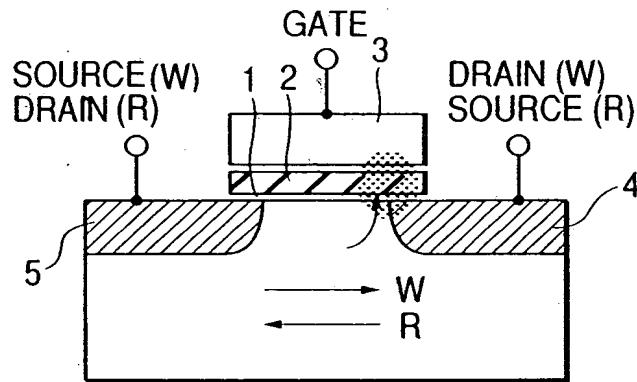
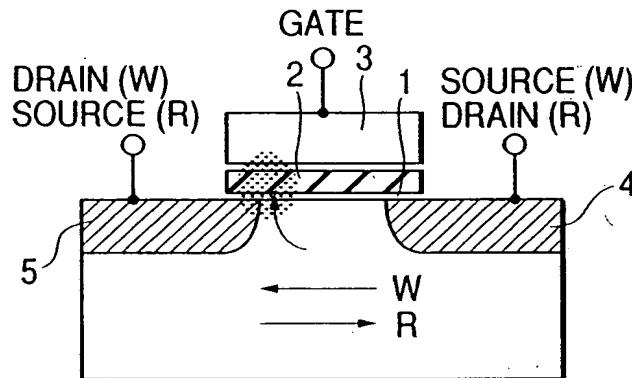
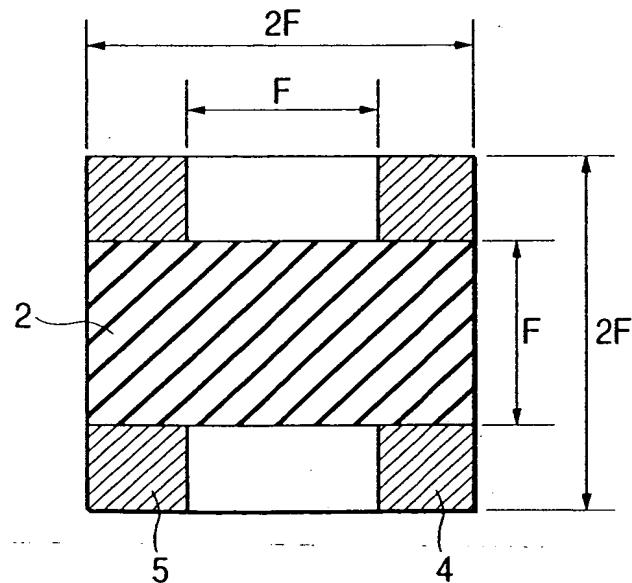
FIG. 45(A) PRIOR ART***FIG. 45(B) PRIOR ART******FIG. 45(C) PRIOR ART***

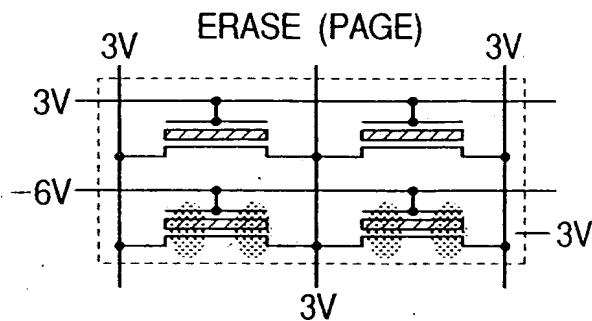
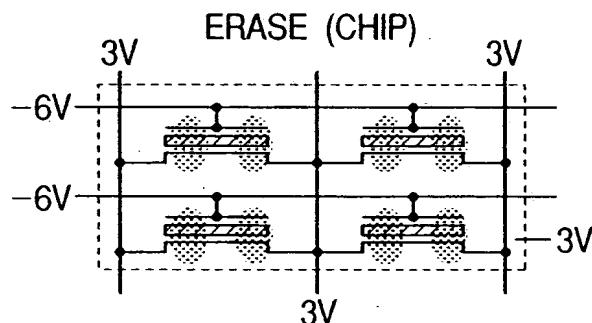
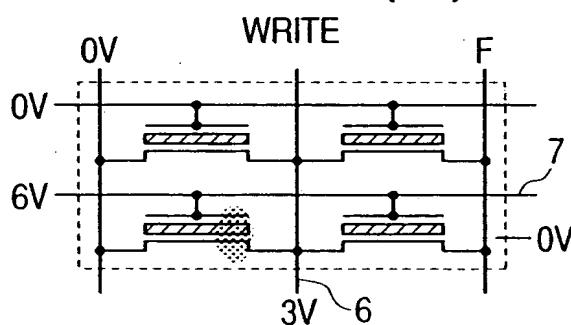
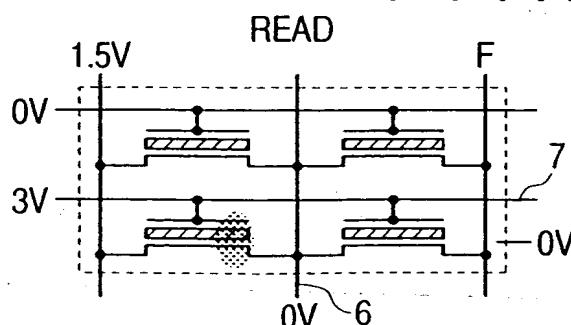
FIG. 46(A) PRIOR ART**FIG. 46(B) PRIOR ART****FIG. 46(C) PRIOR ART****FIG. 46(D) PRIOR ART**

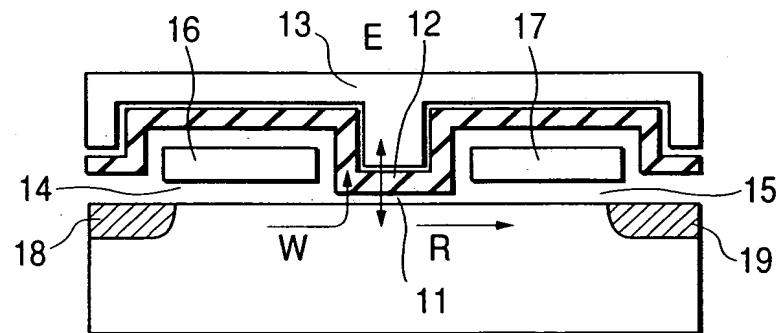
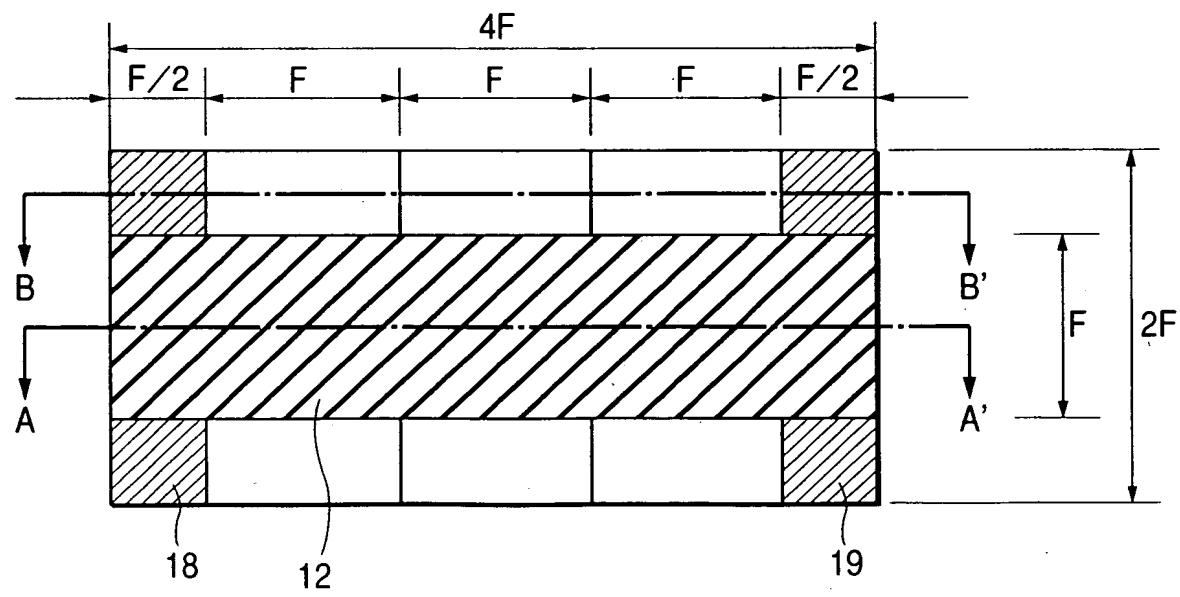
FIG. 47(A)**FIG. 47(B)**

FIG. 48(A)

ERASE (PAGE)

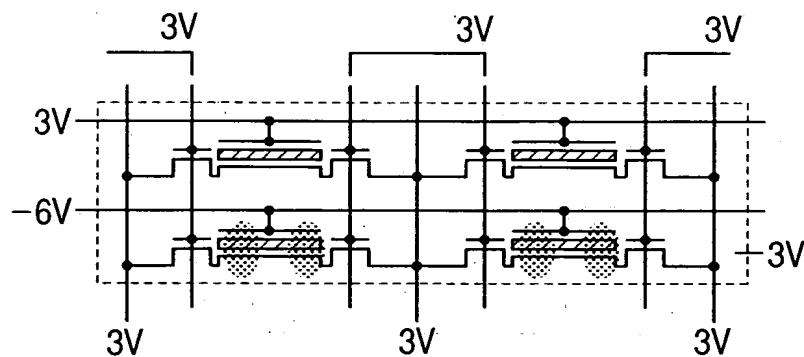


FIG. 48(B)

WRITE

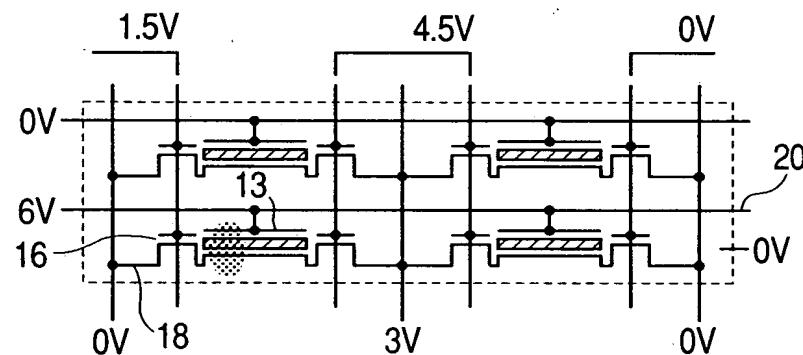


FIG. 48(C)

READ

